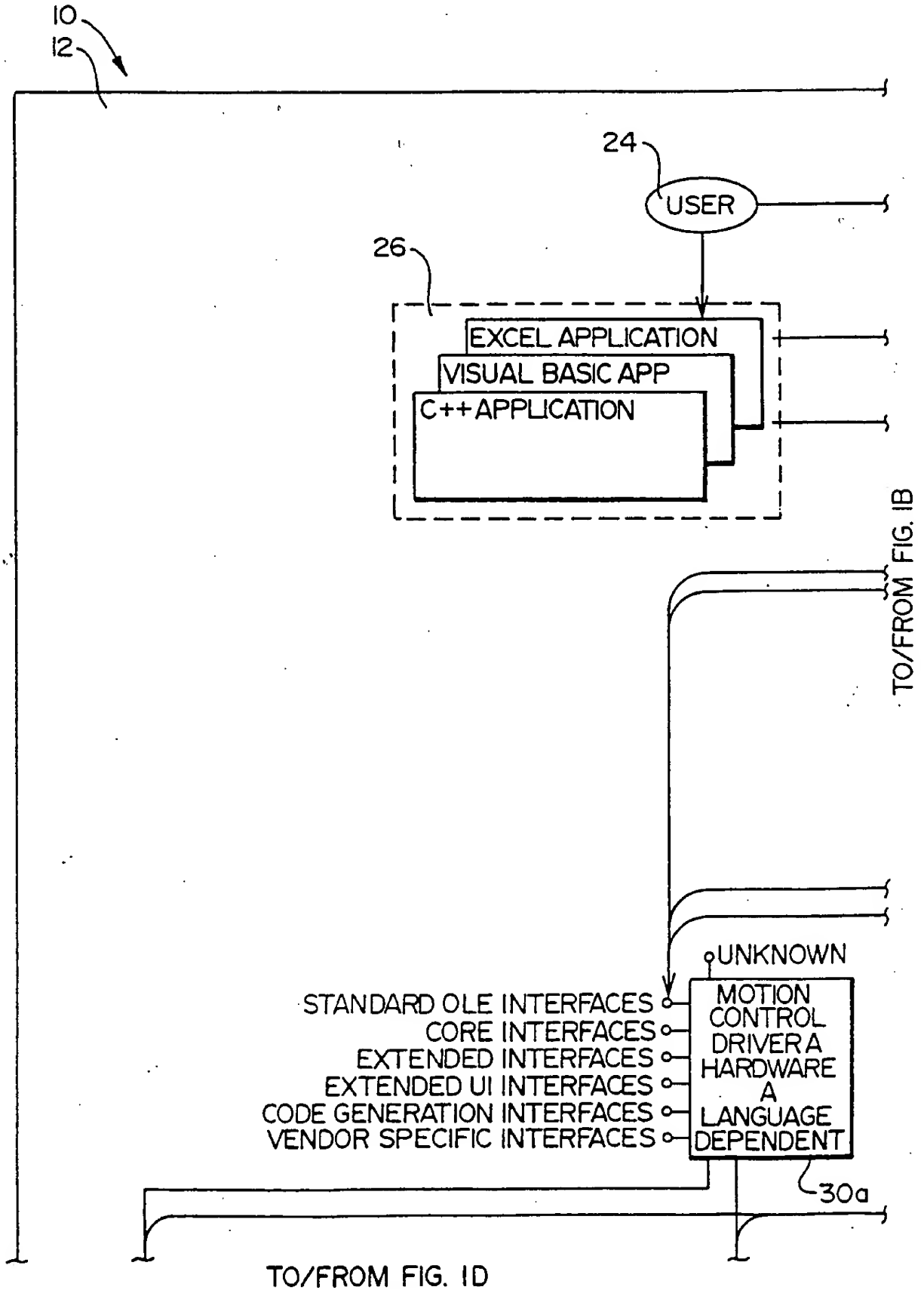
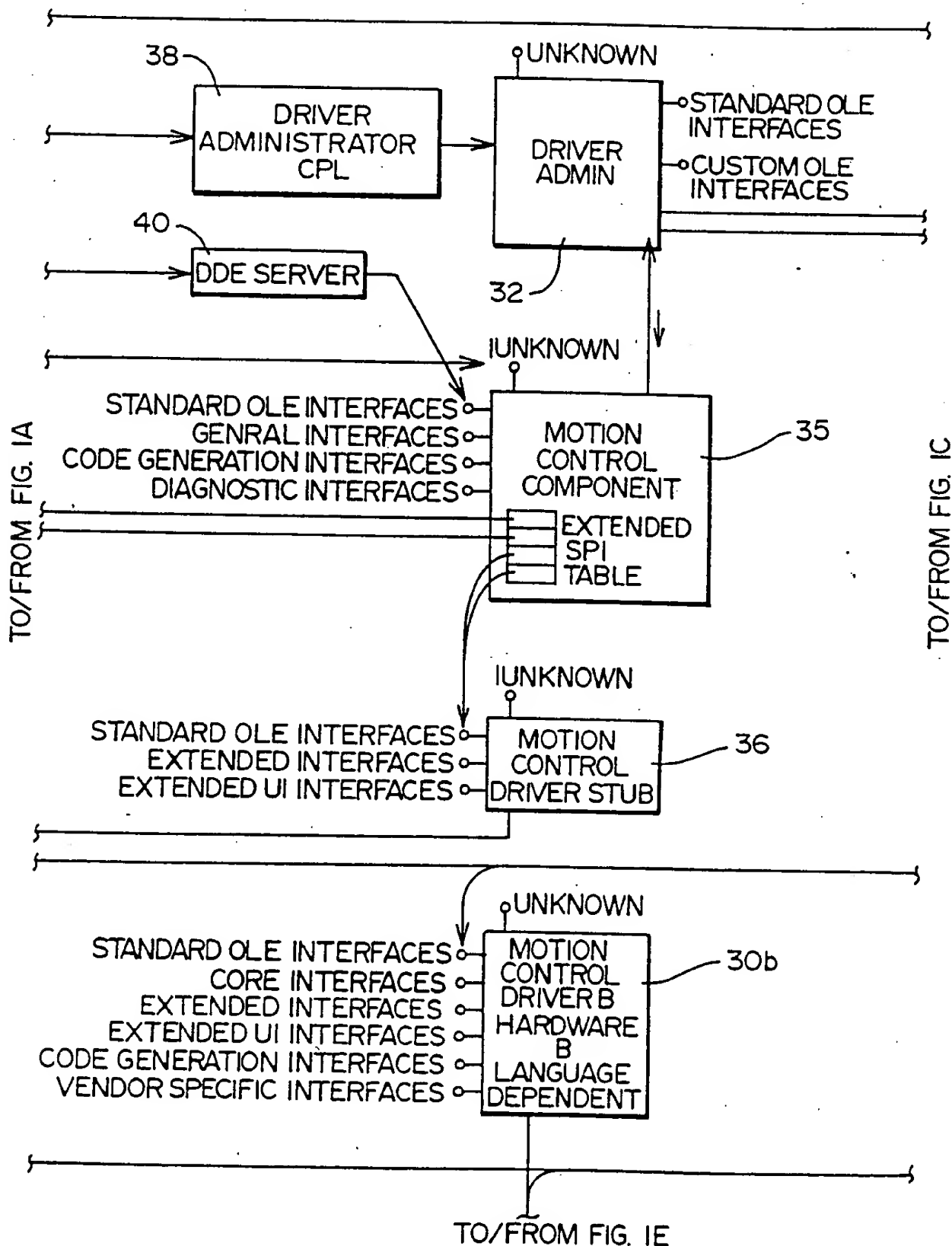


FIG. 1A



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FIG. 1B



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FIG. 1D

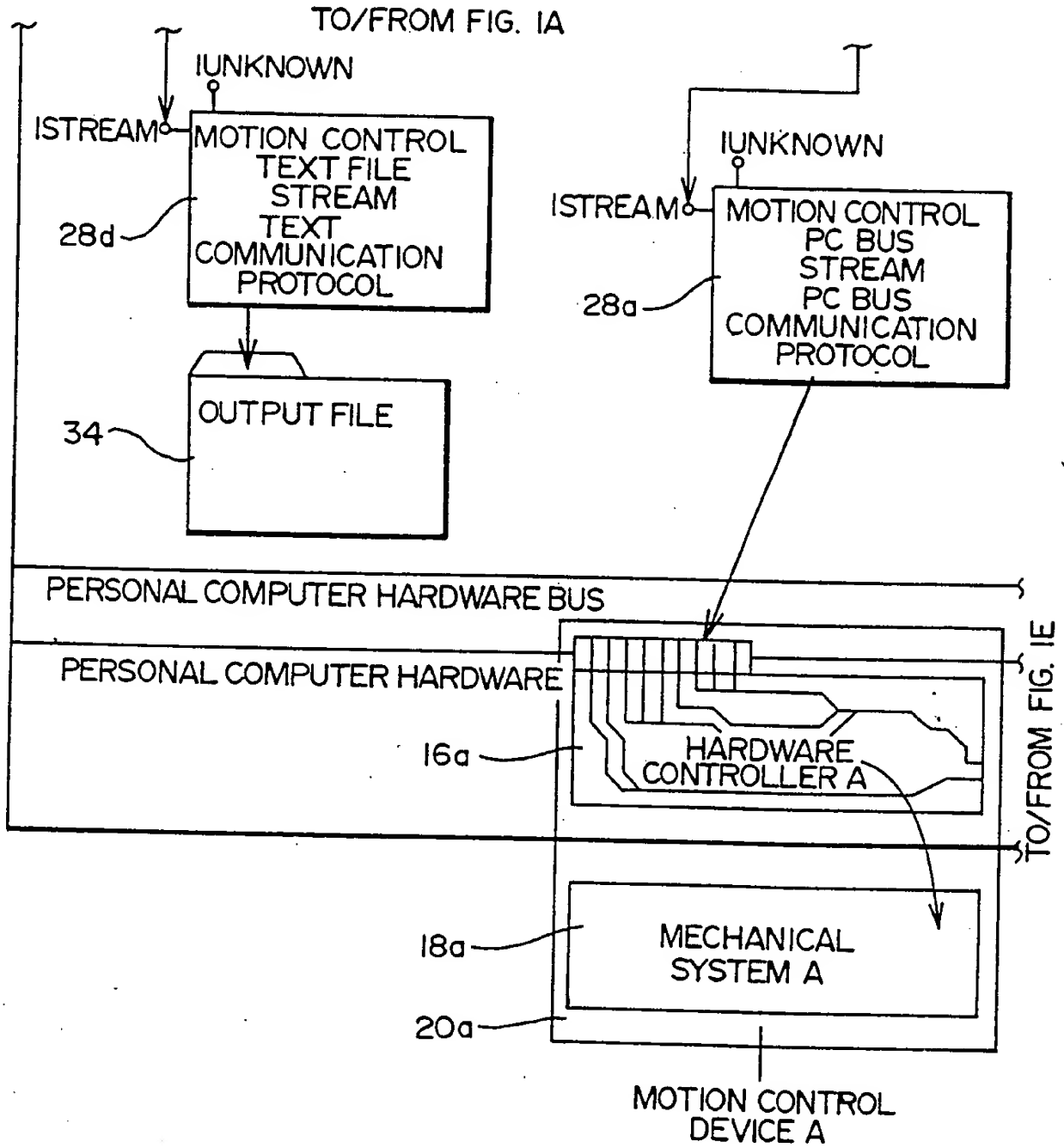
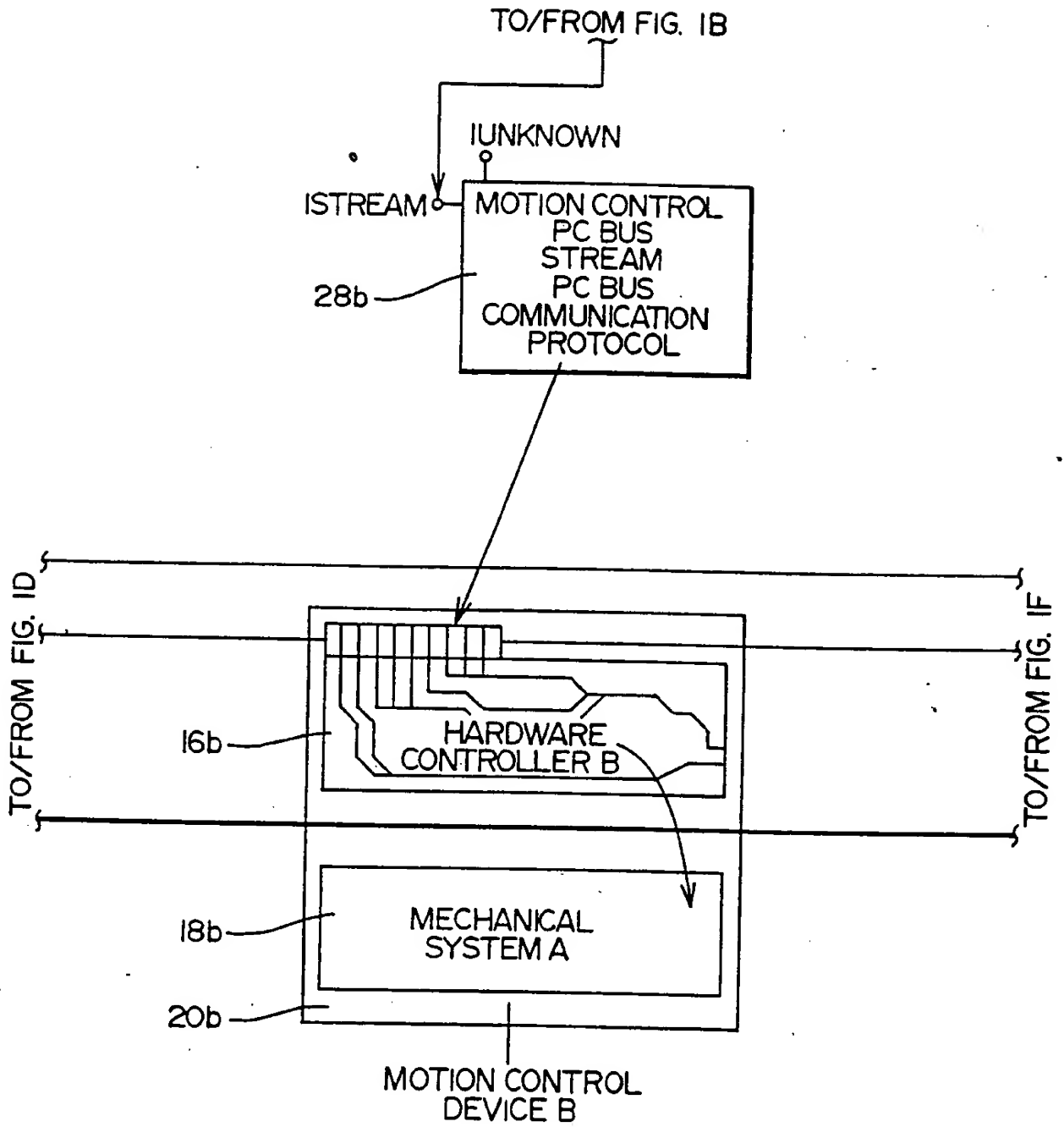
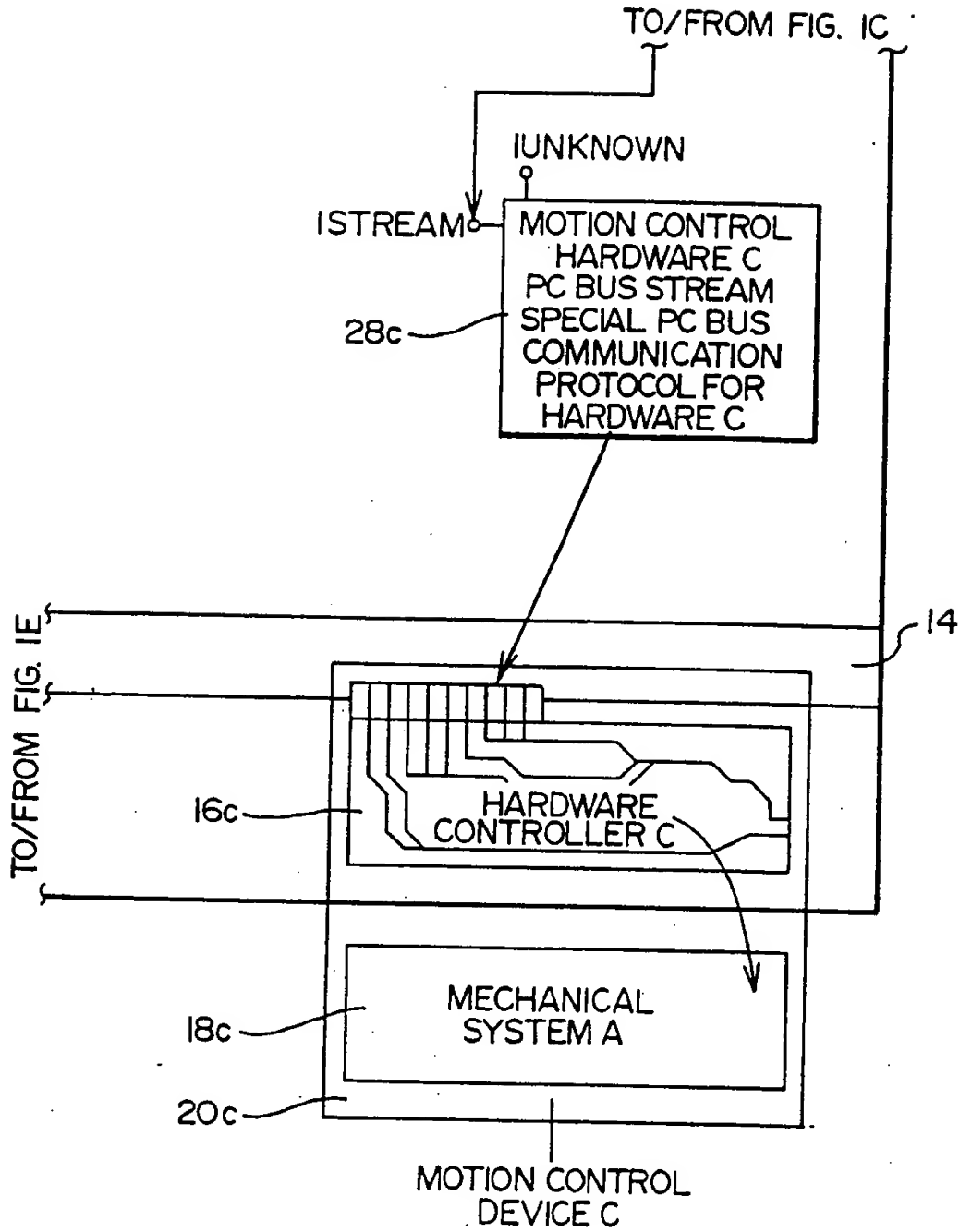


FIG. 1E



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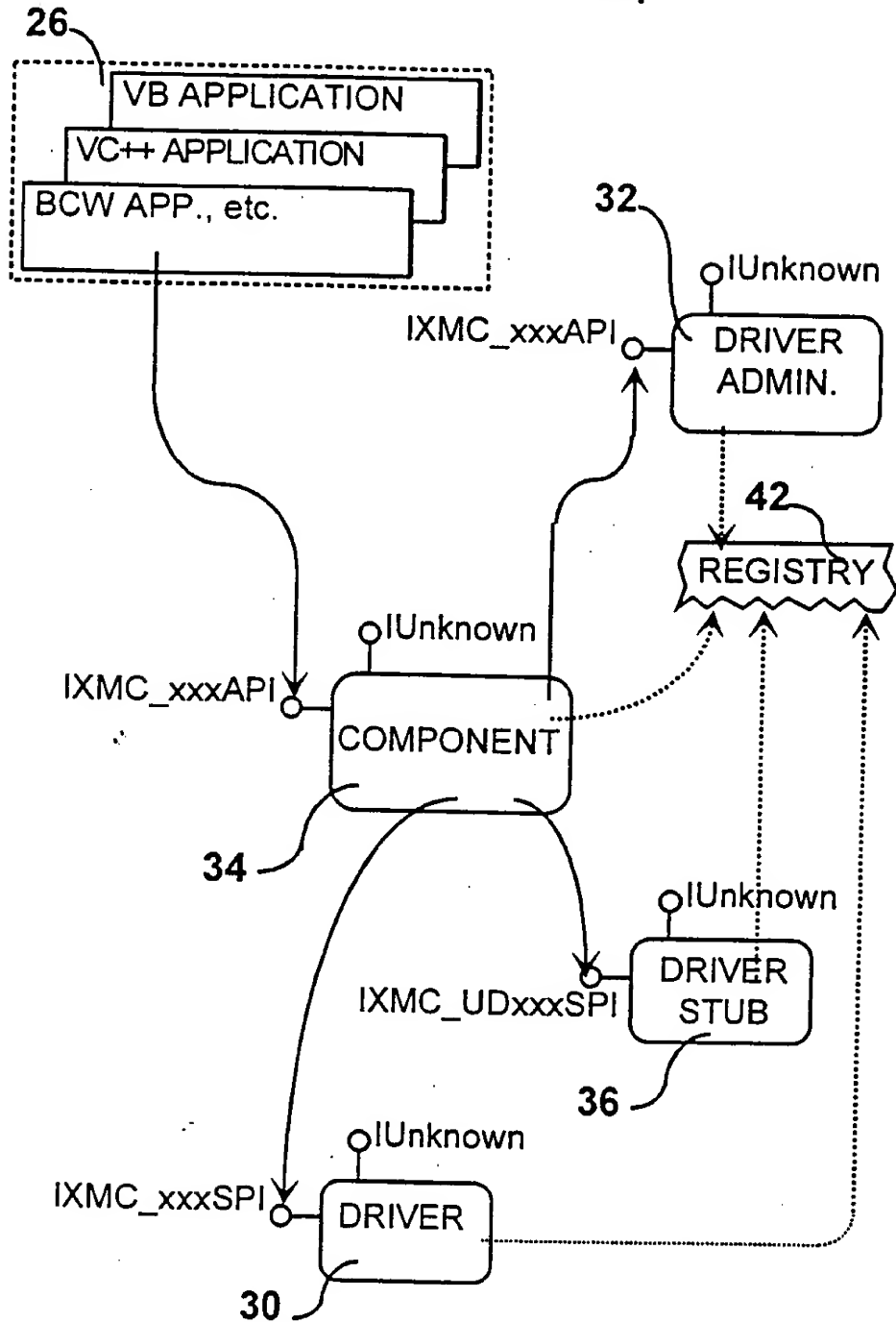
FIG. 1F



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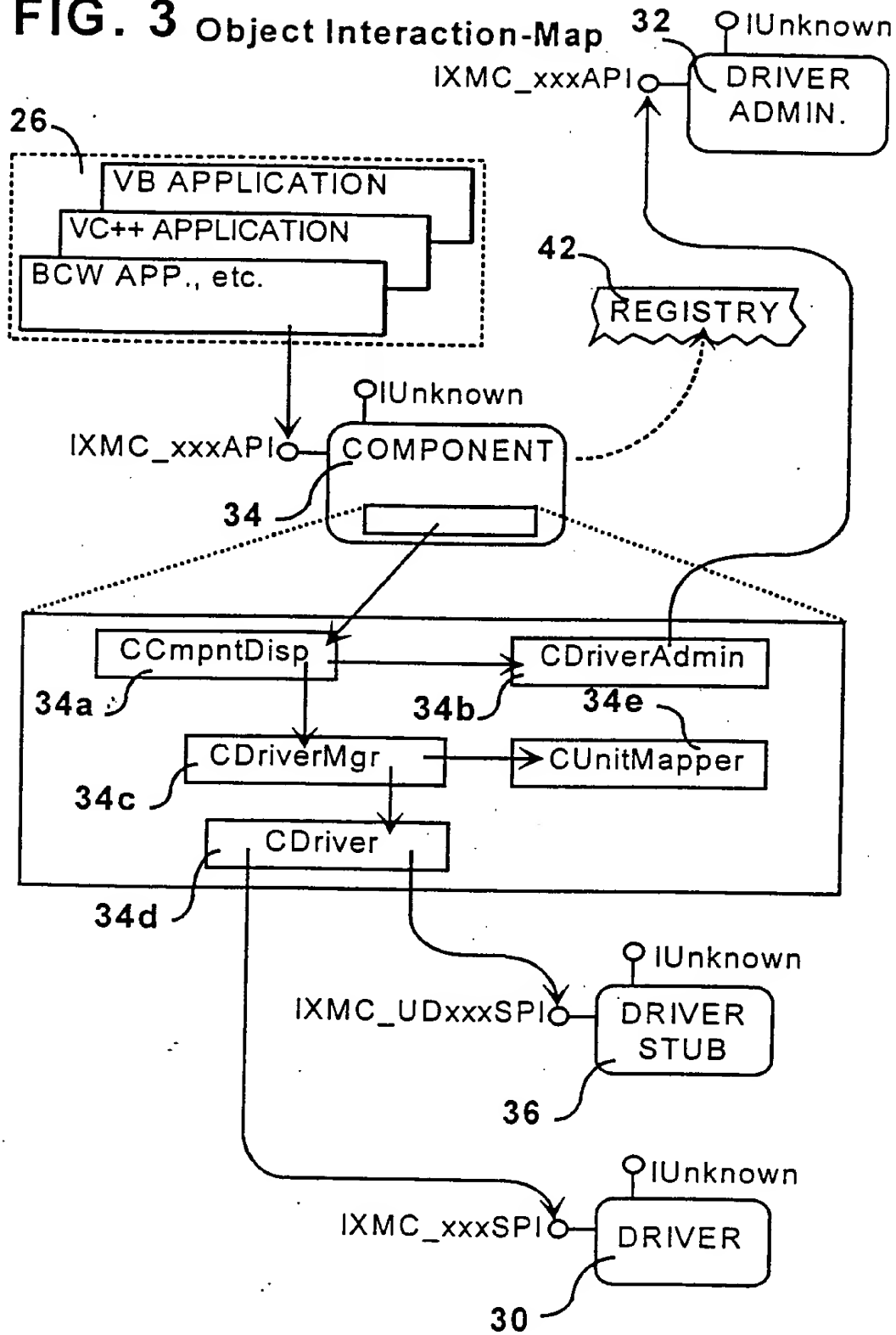
FIG. 2 Module Interaction-Map



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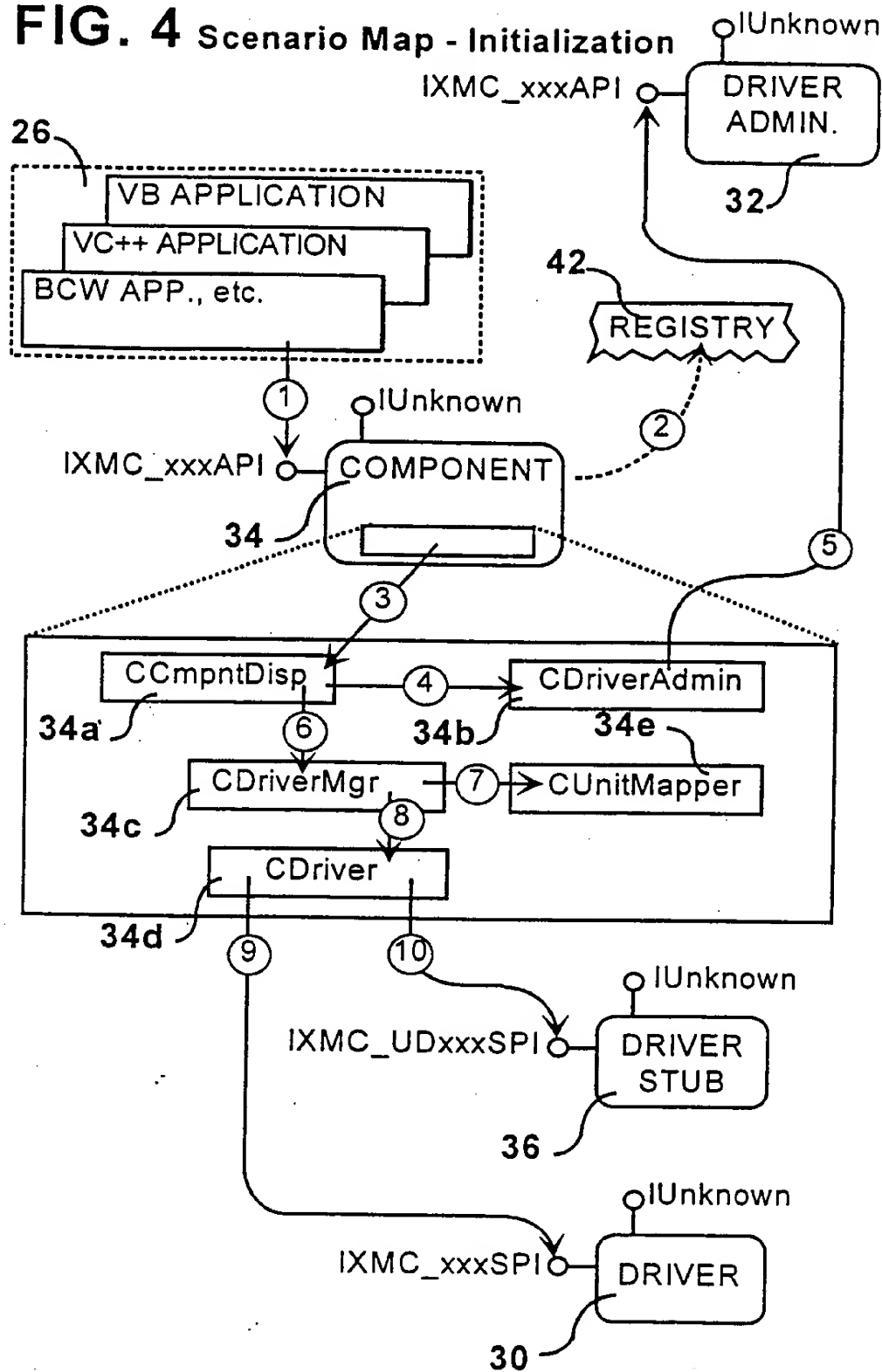
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FIG. 3 Object Interaction-Map



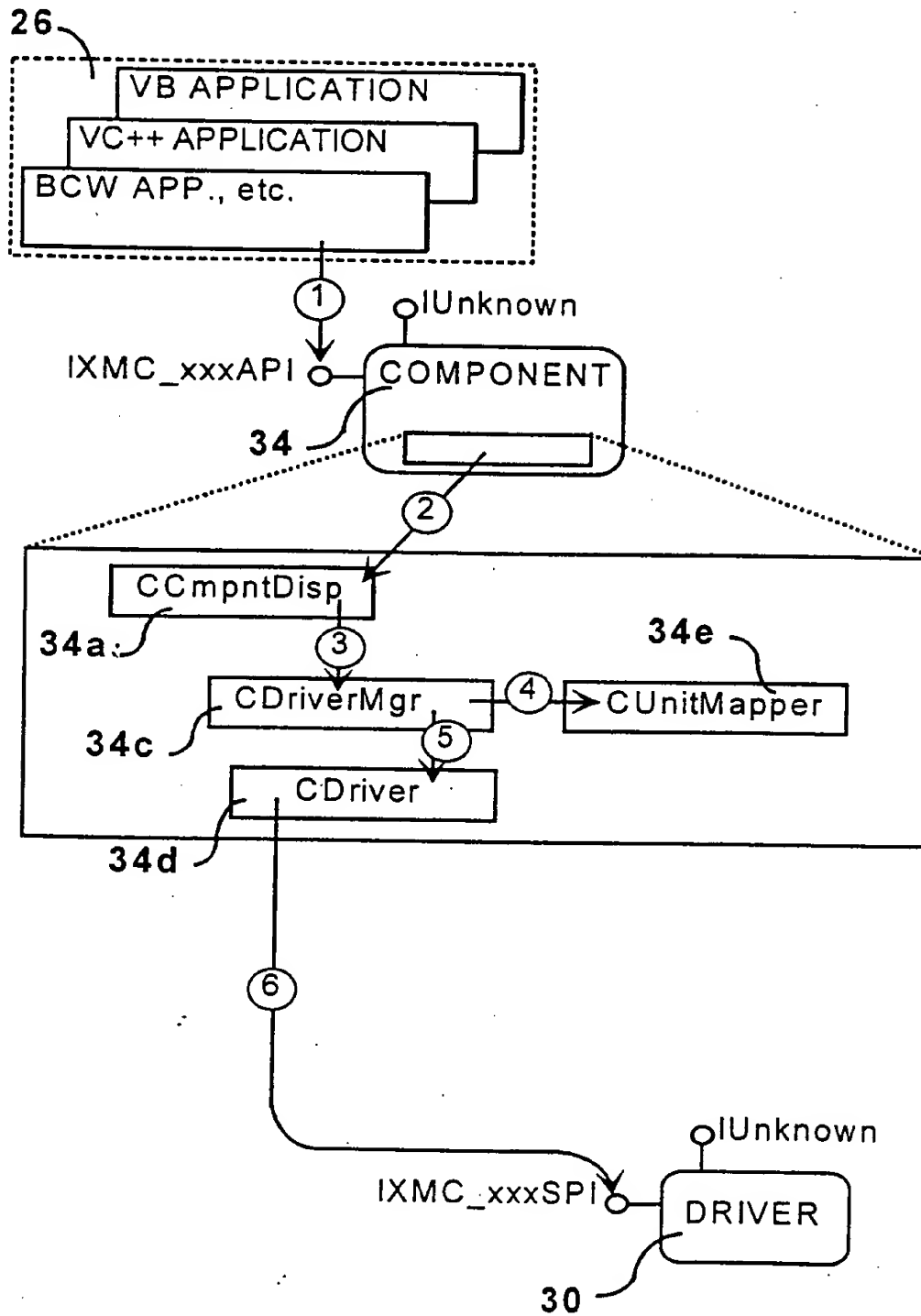
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FIG. 4 Scenario Map - Initialization



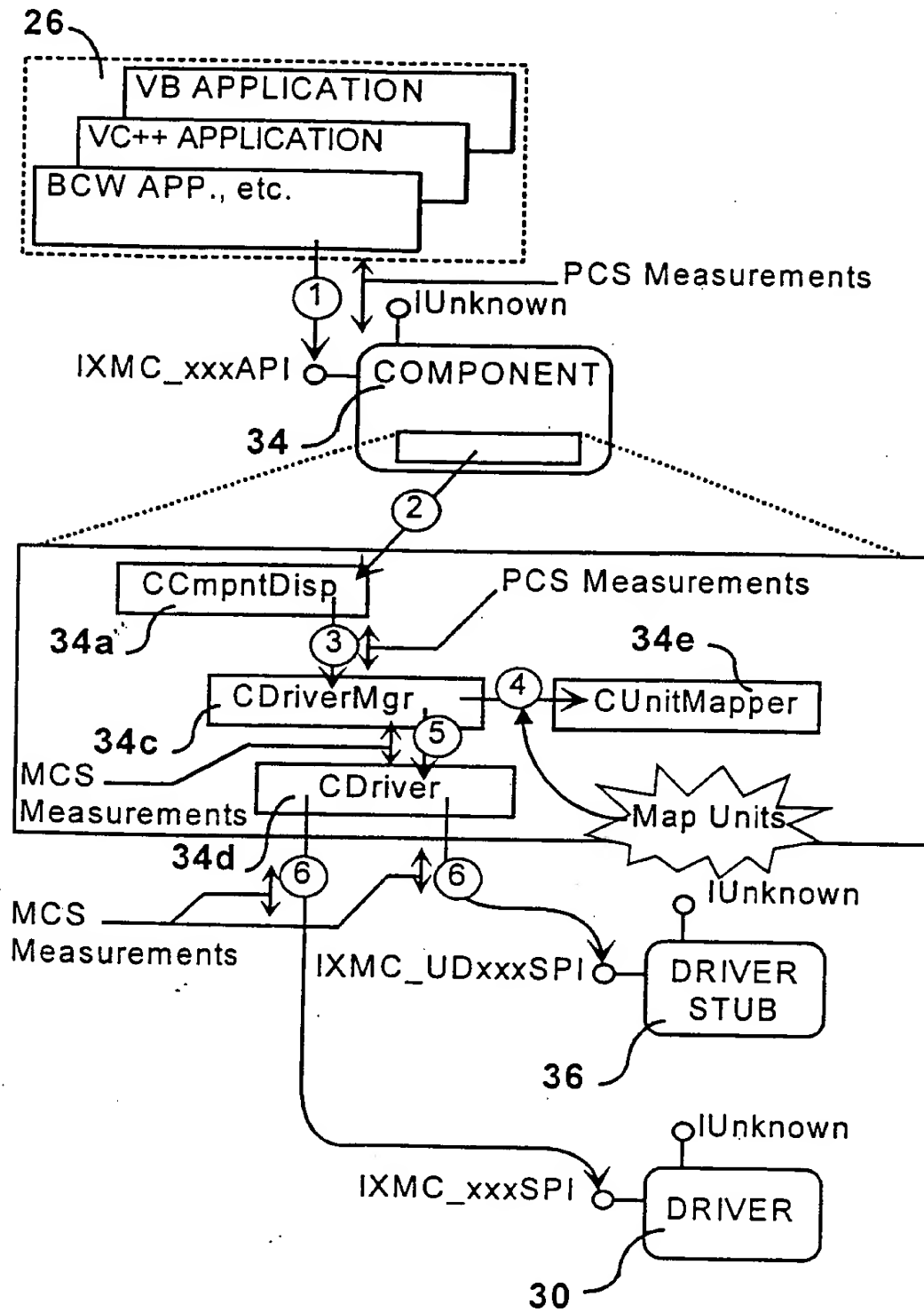
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FIG. 5 Scenario Map - Core SPI Operation



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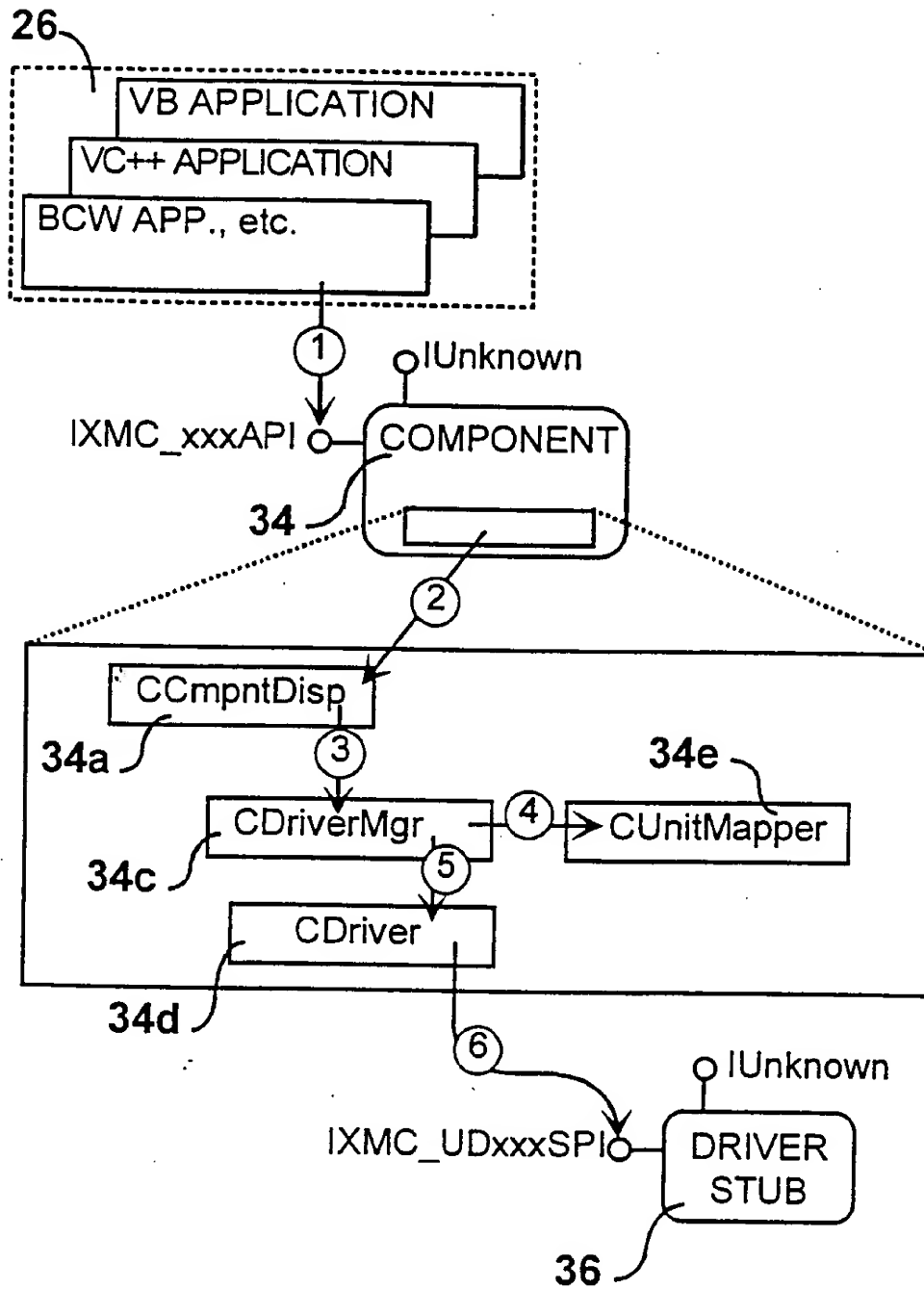
FIG. 6 Scenario-Map - Unit Mapping



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FIG. 7 Scenario-Map - Extended SPI Operation



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FIG. 8 Scenario-Map - Clean-up.

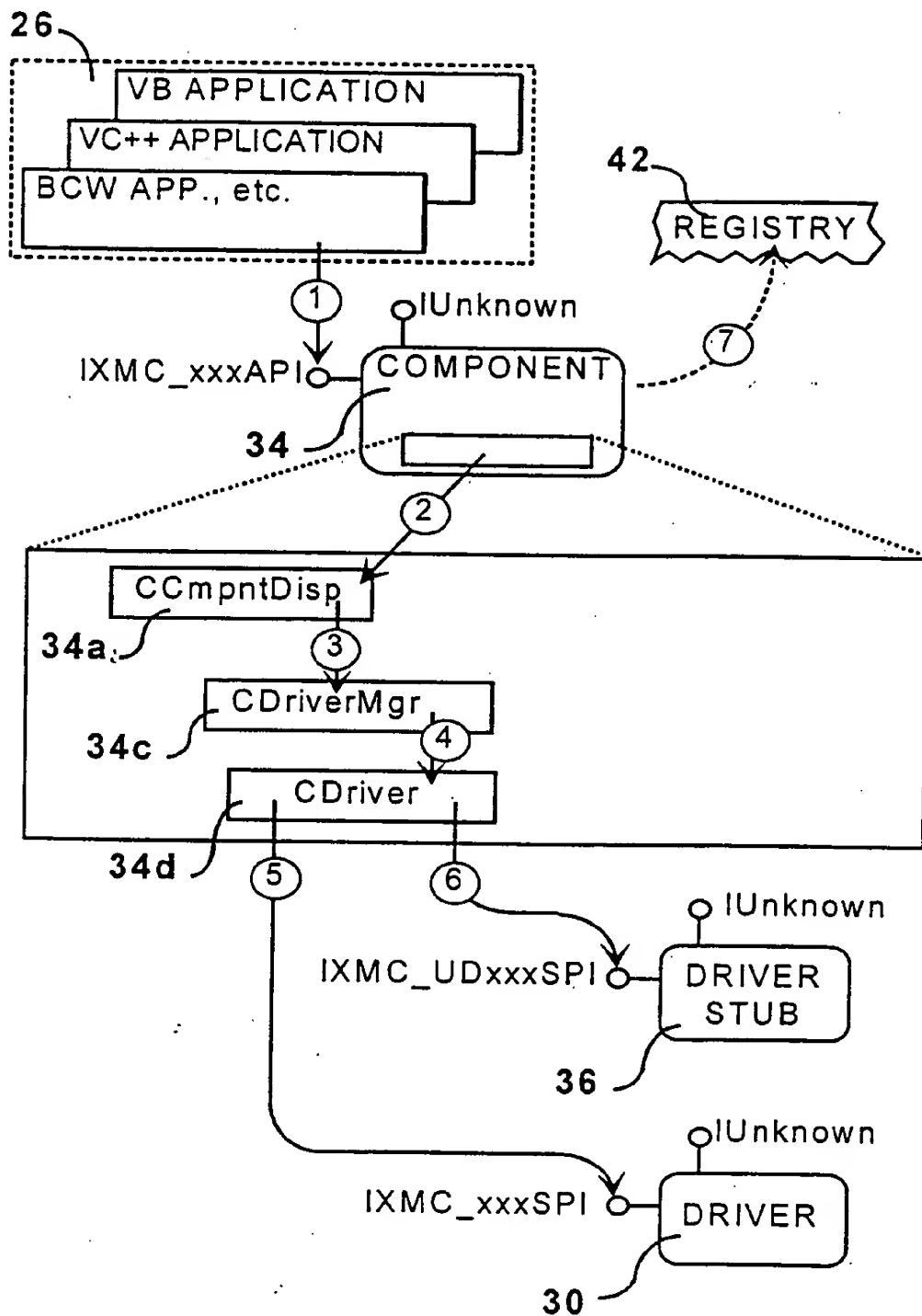
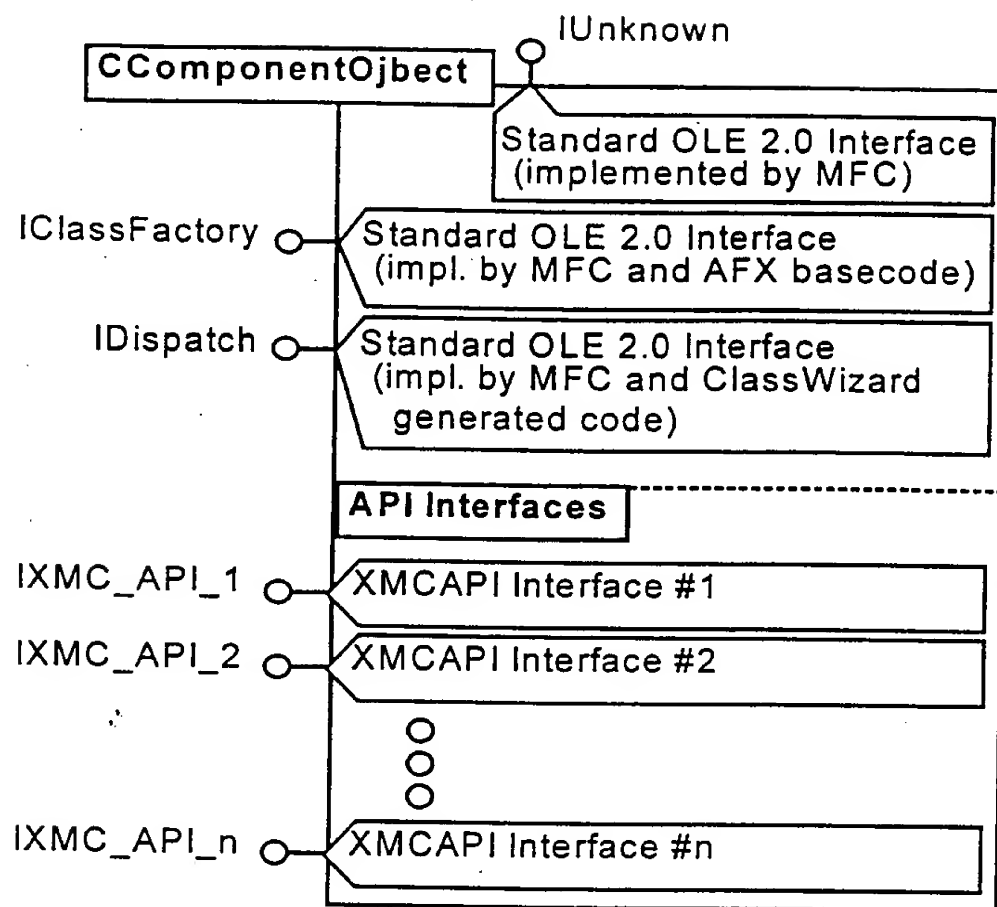
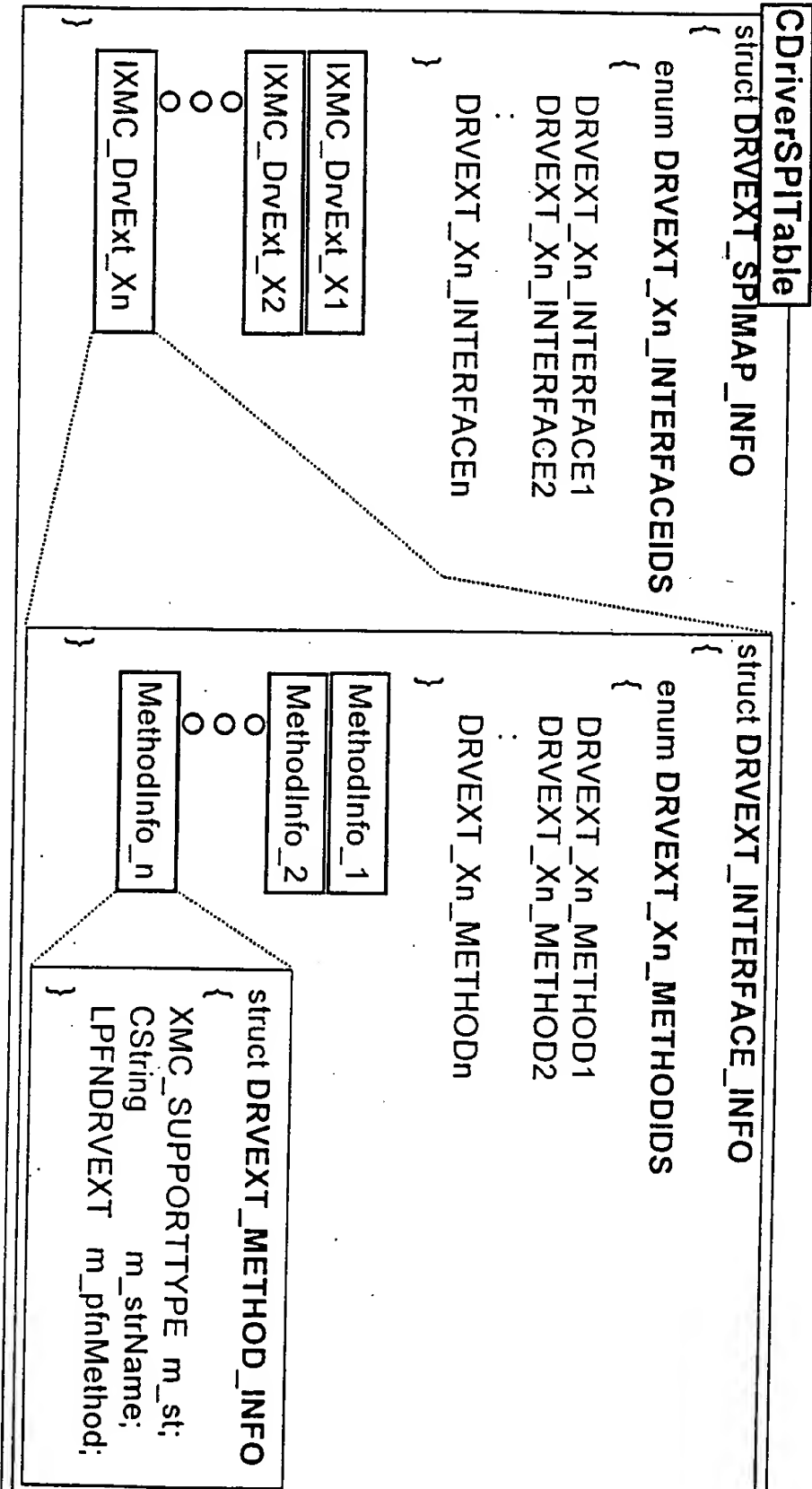


FIG. 9 Interface-Map

[illegible]

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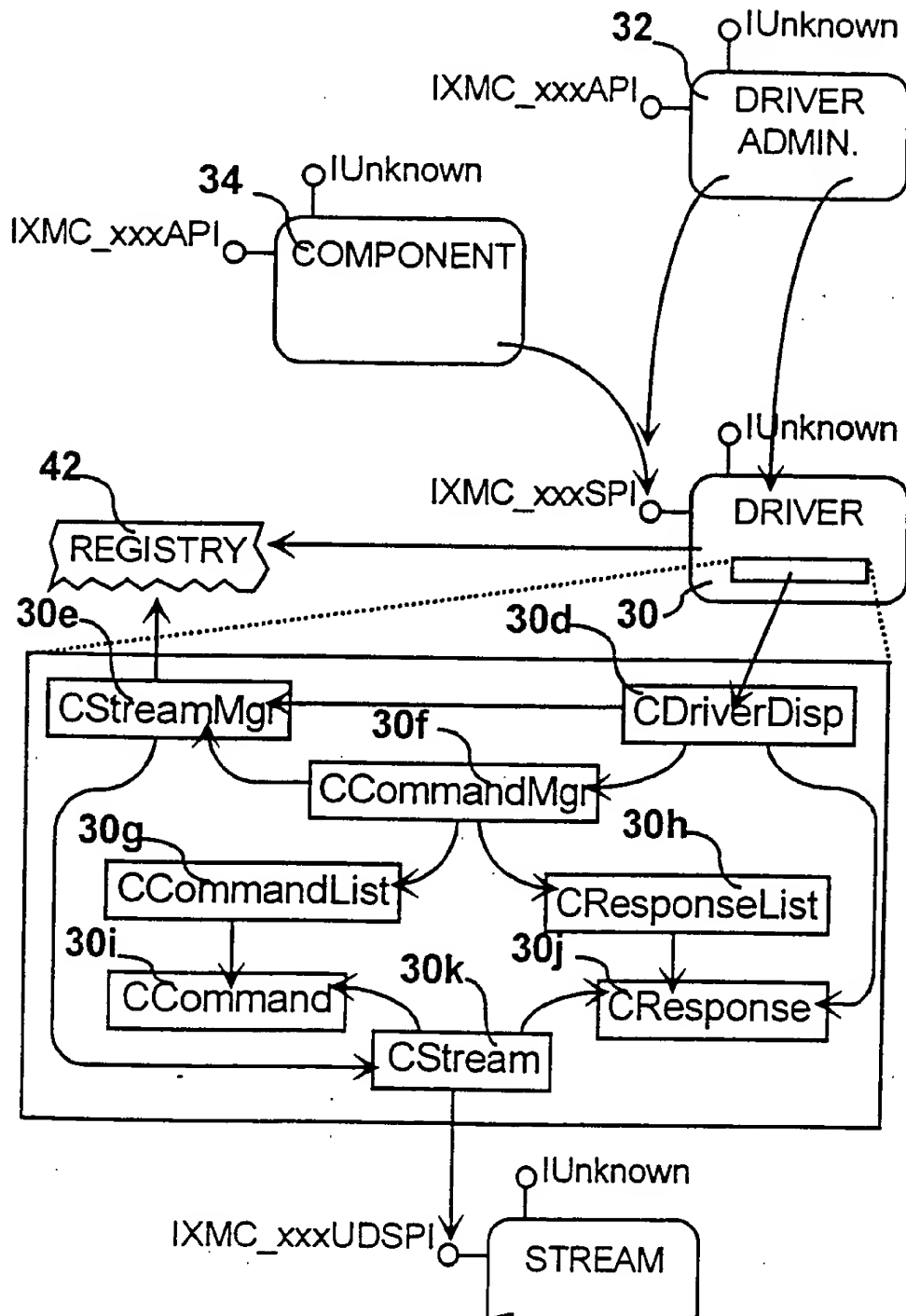
FIG. 10 CDriver class with XMCSPi table



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FIG. 12 Object Interaction-Map



```

graph TD
    DA[DRIVER ADMIN.] -- 32 --> IUnknown1[IUnknown]
    DA -- IXMC_xxxAPI --> IXMC_xxxAPI
    D[DRIVER] -- IUnknown --> IUnknown2[IUnknown]
    D -- 30 --> IXMC_xxxSPI[IXMC_xxxSPI]
    DA -- 1 --> IXMC_xxxSPI
    IXMC_xxxSPI -.-> 2((2))
    2 -.-> R[REGISTRY]
    style R fill:#fff,stroke:#000,stroke-width:1px,stroke-dasharray: 5 5
  
```

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FIG. 15 Scenario-Map - Adding a Stream

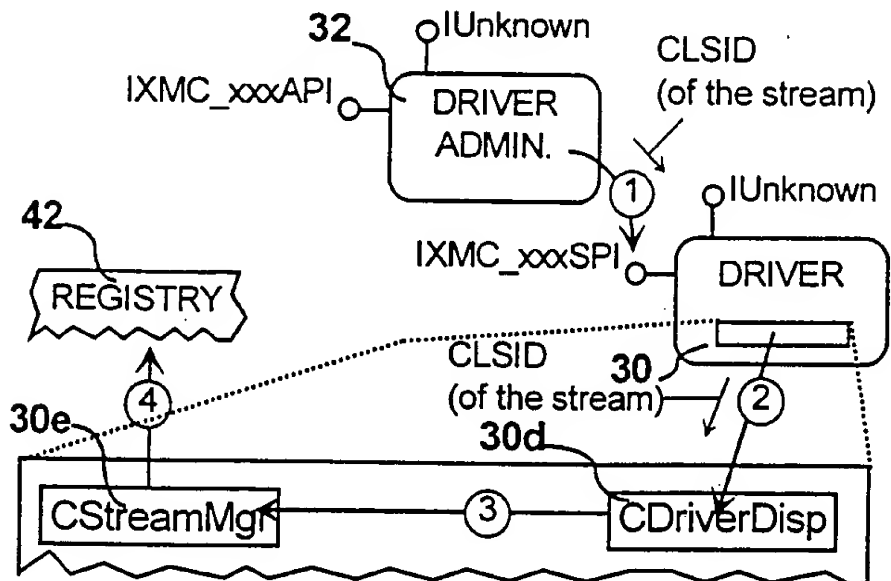
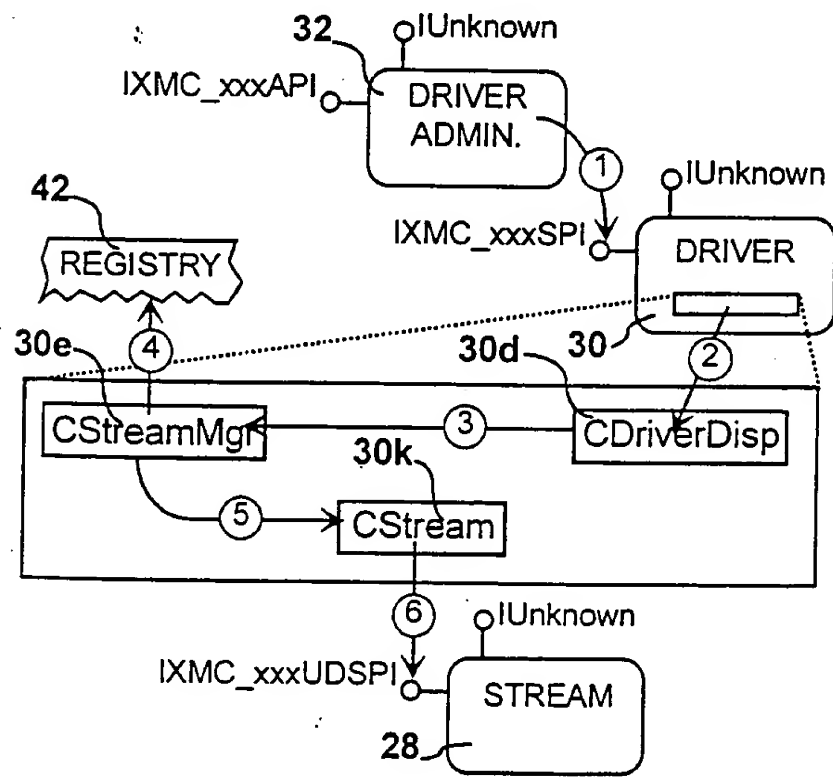


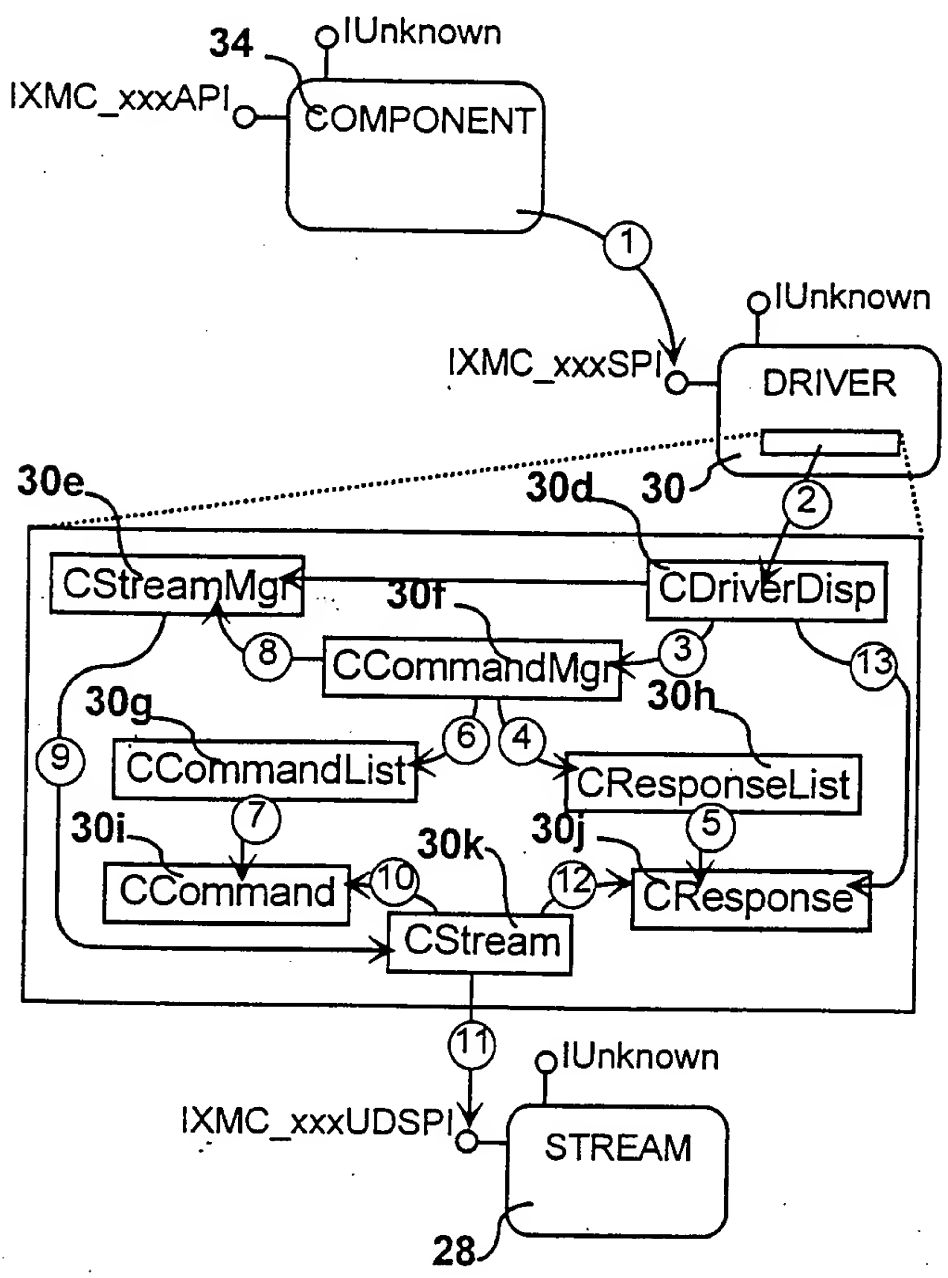
FIG. 16 Scenario-Map - Query Operation



[illegible]

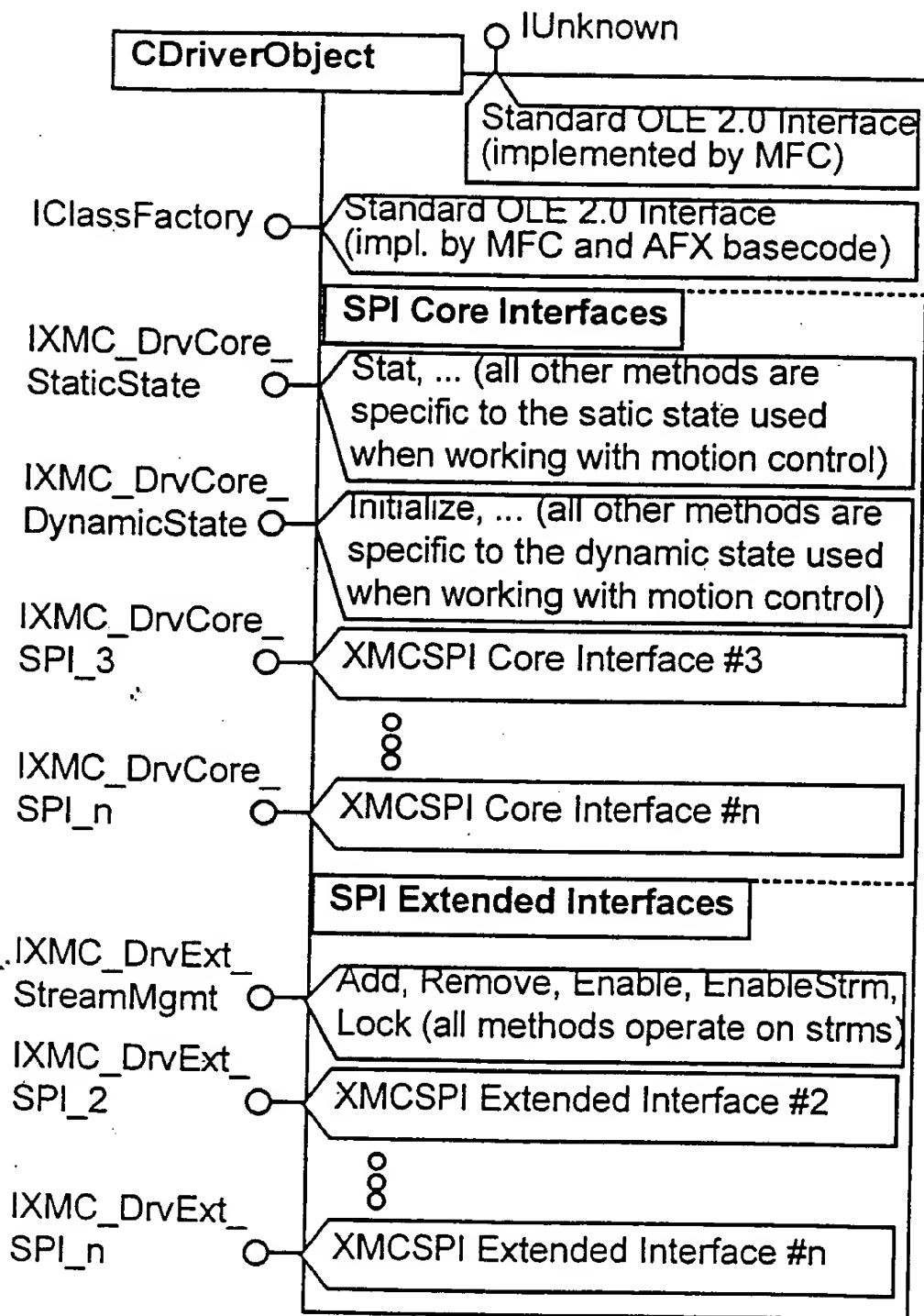
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FIG. 19 Scenario-Map - Command Operations



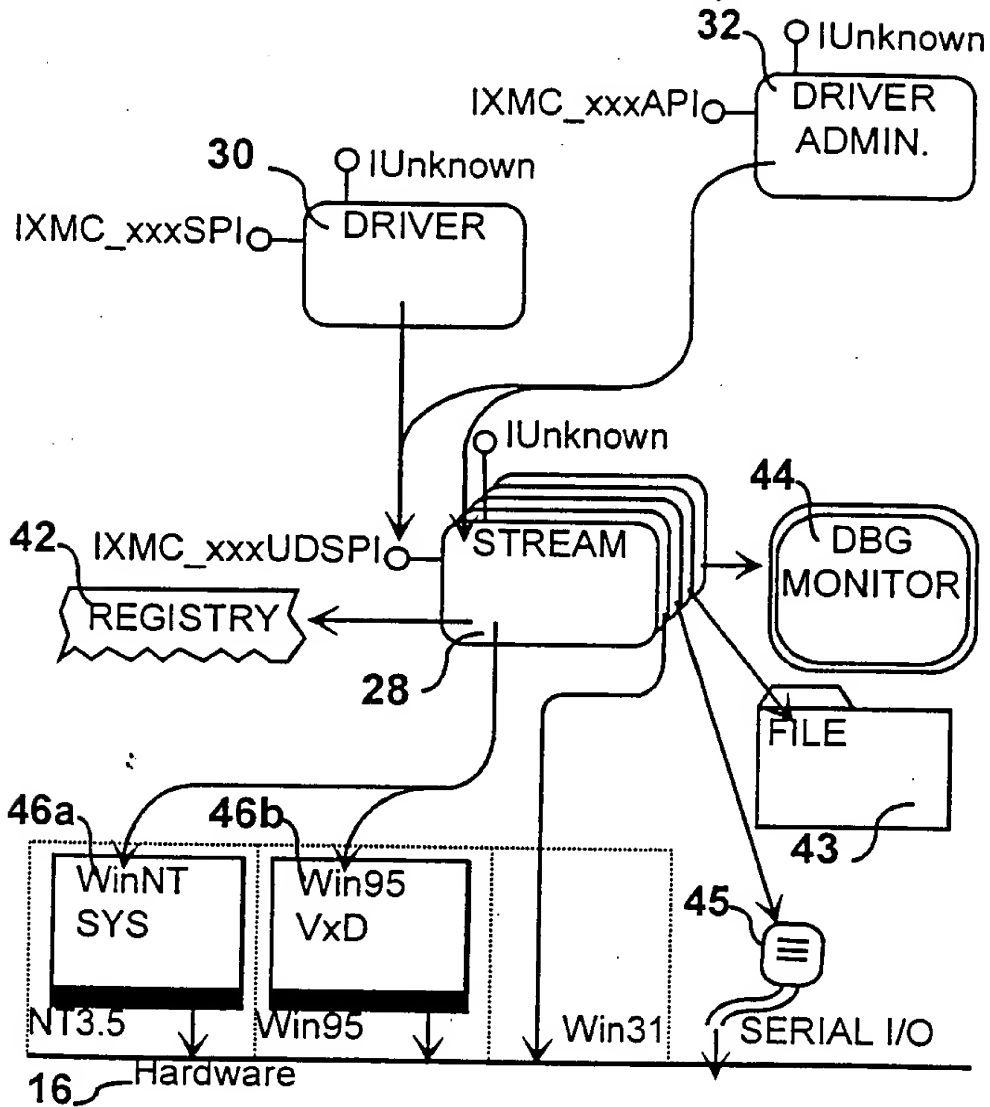
000000-000000

FIG. 21 Interface-Map



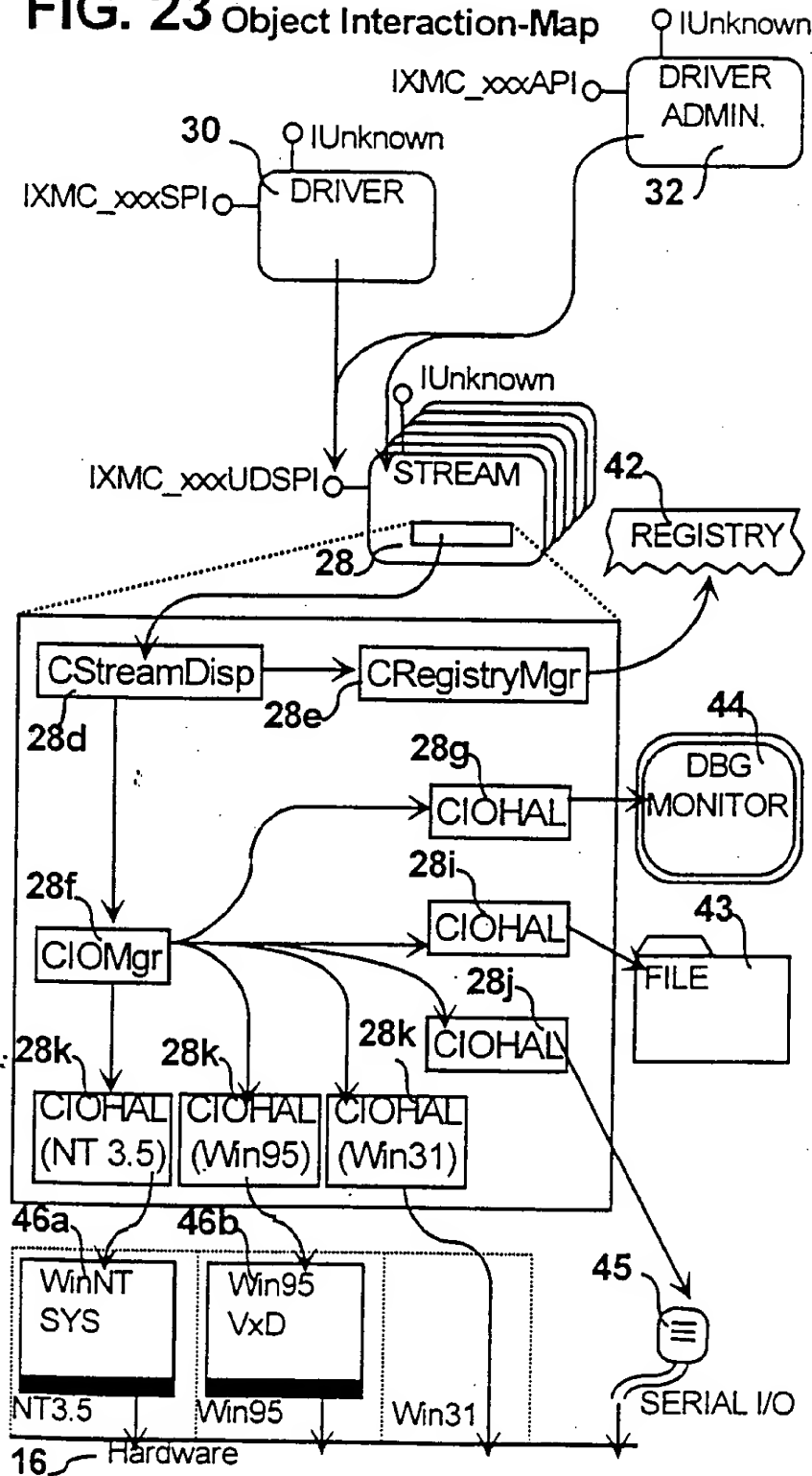
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FIG. 22 Module Interaction-Map



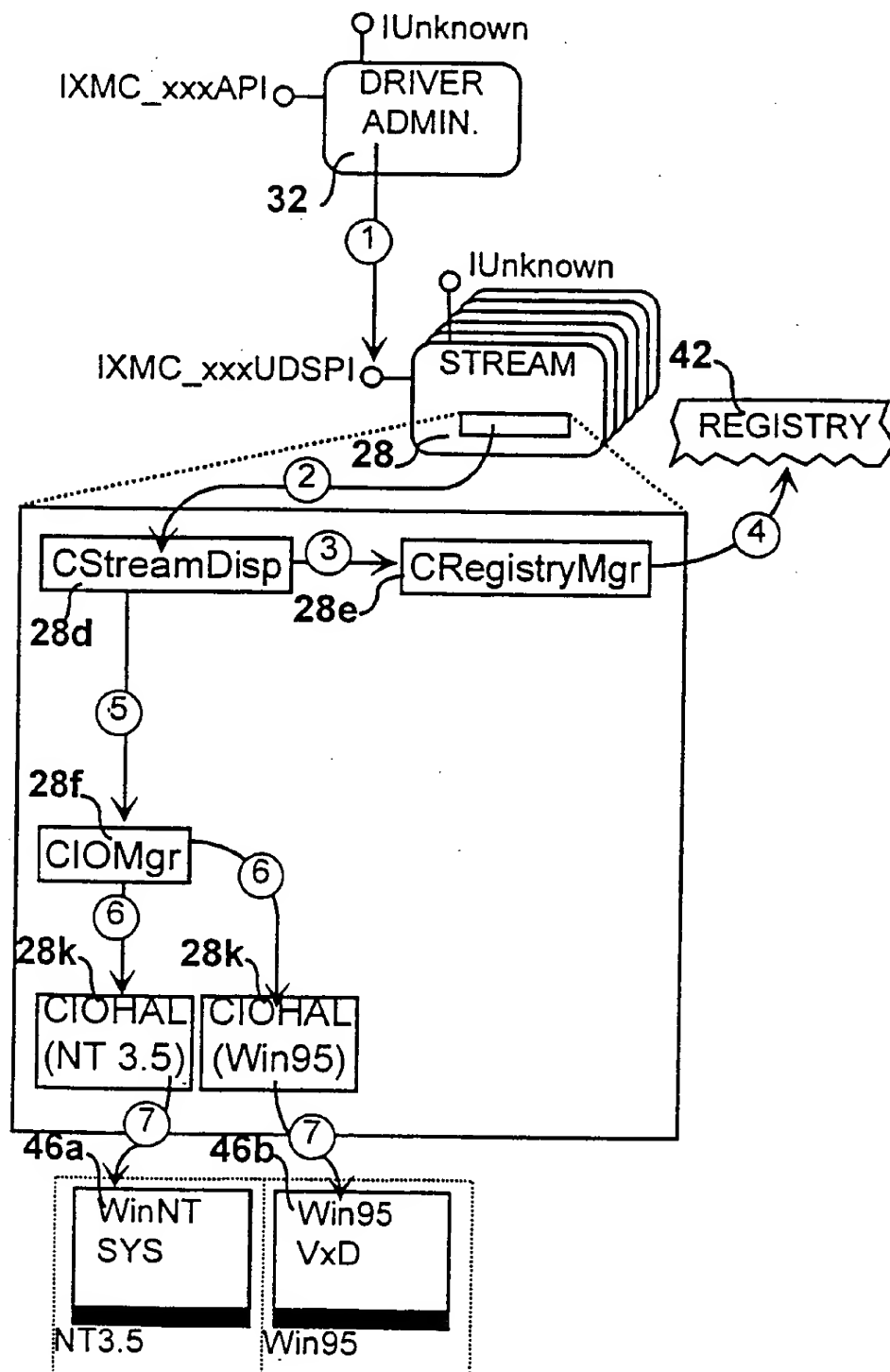
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FIG. 23 Object Interaction-Map



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FIG. 24 Scenario-Map - Initialization



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FIG. 25 Scenario-Map - Registration

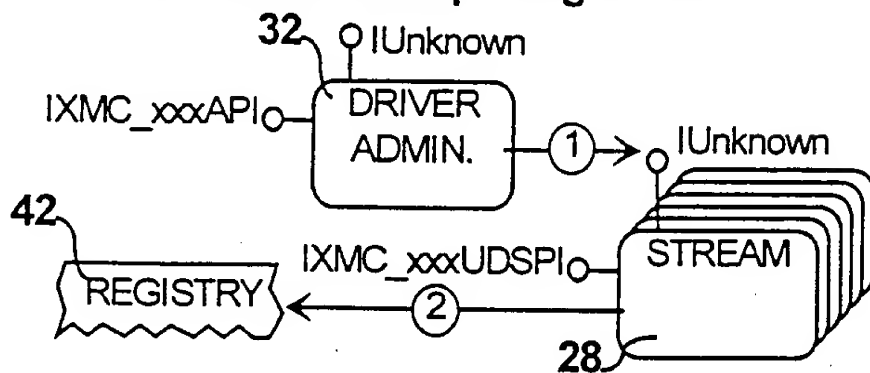
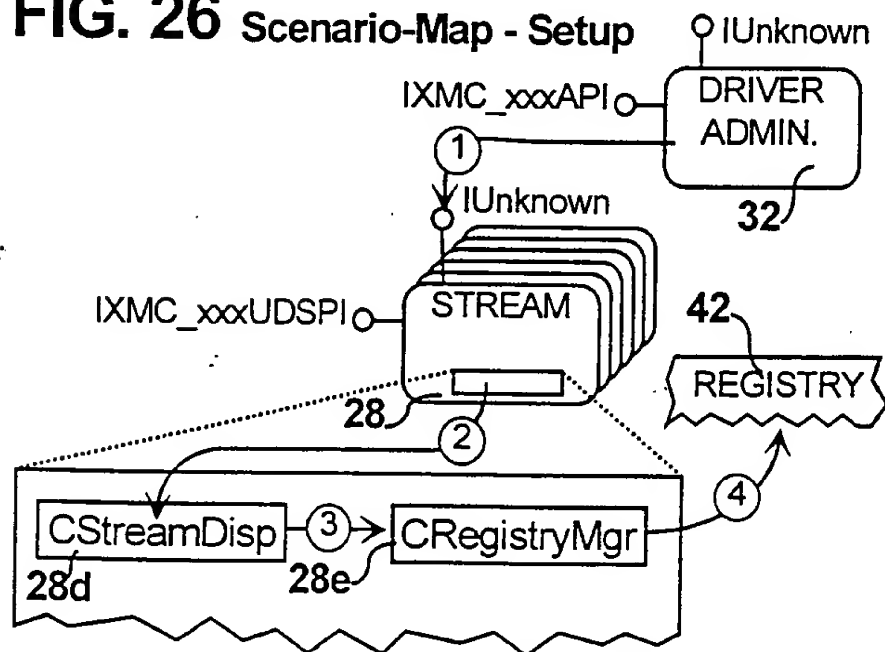


FIG. 26 Scenario-Map - Setup



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FIG. 27 Scenario-Map - Clean-up

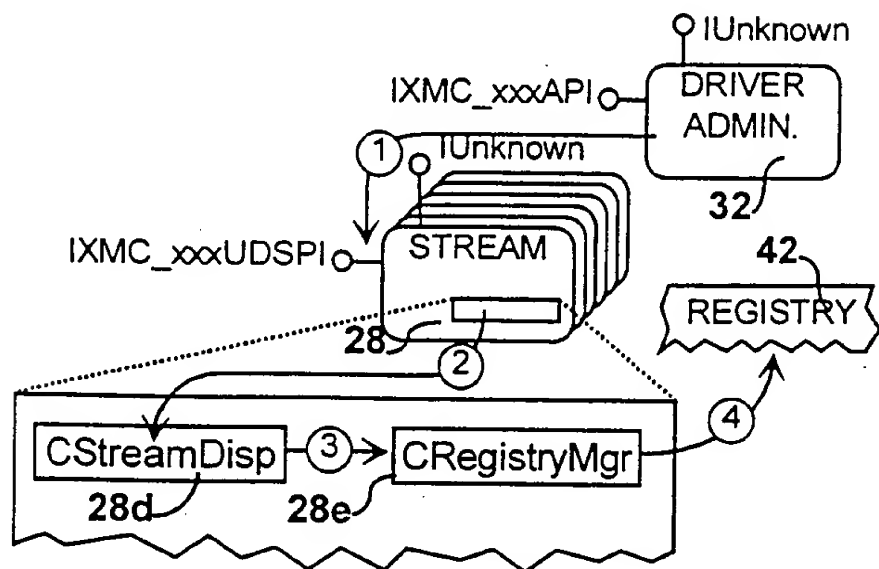


FIG. 28 Scenario-Map - Initialization (Drv)

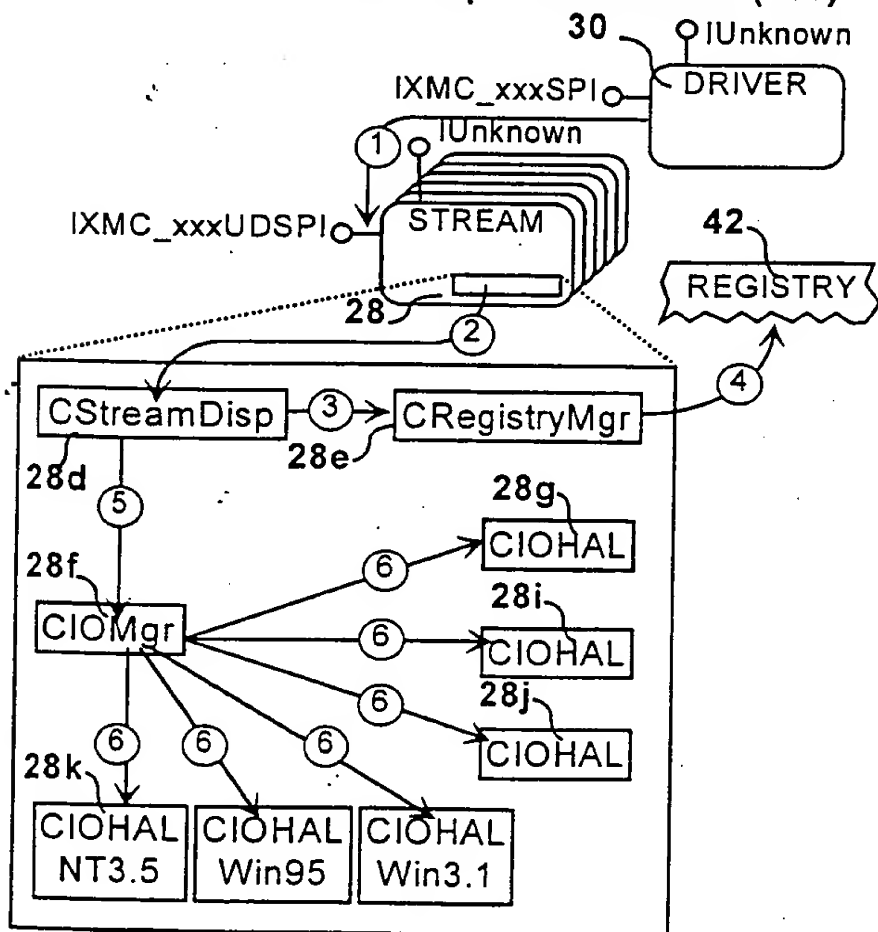
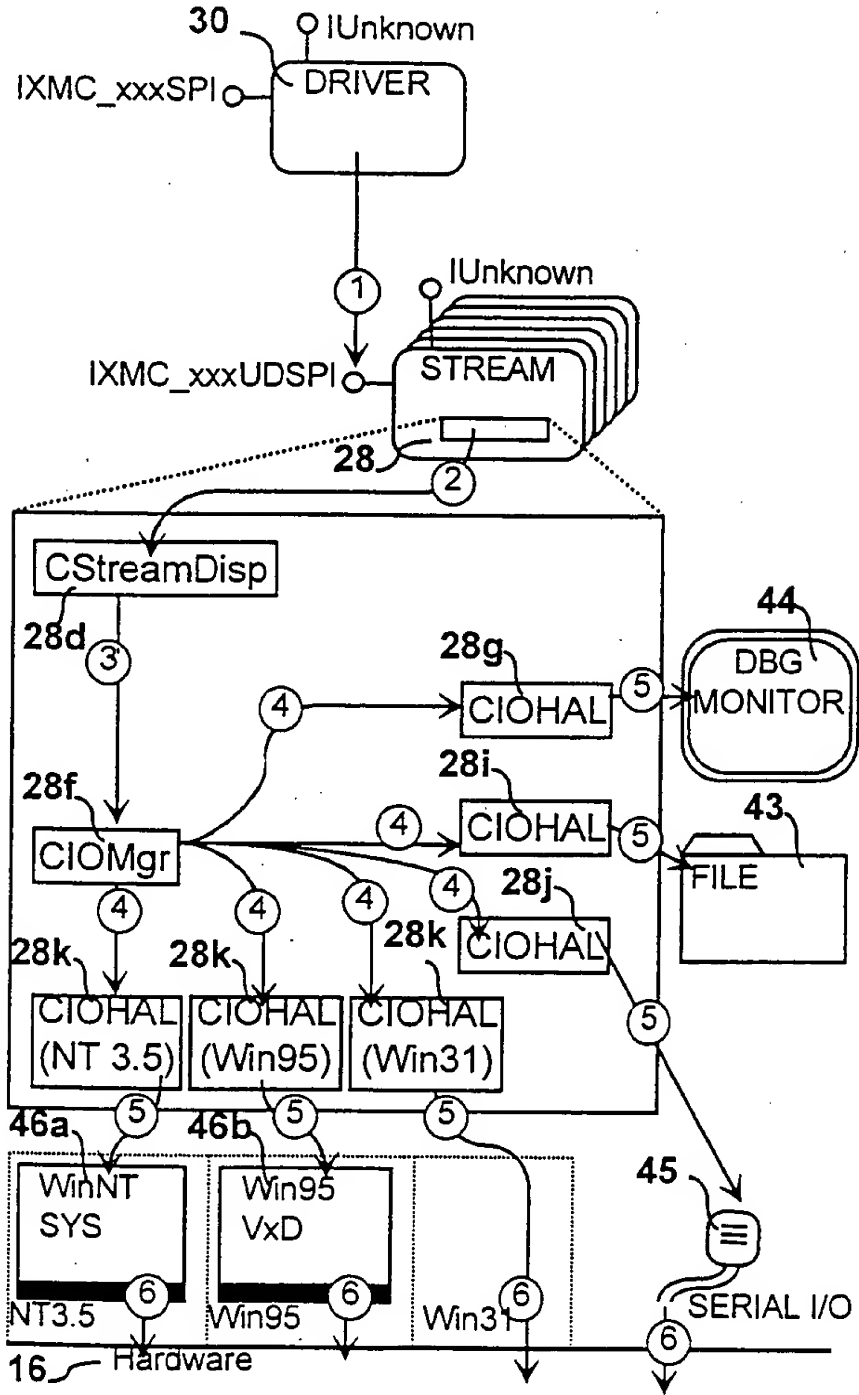


FIG. 30 Scenario-Map - Writing Data



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FIG. 31 Scenario-Map - Reading Data

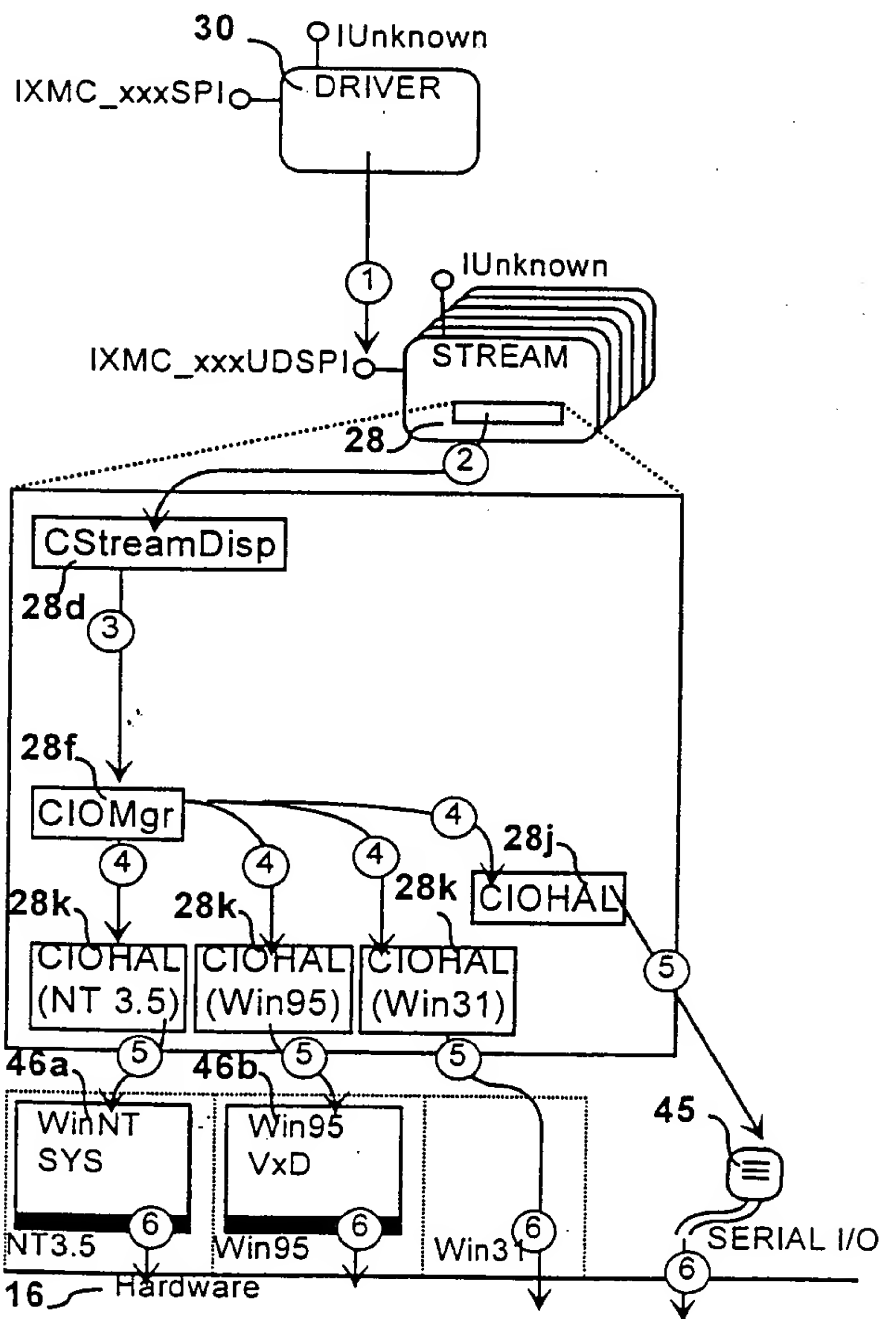
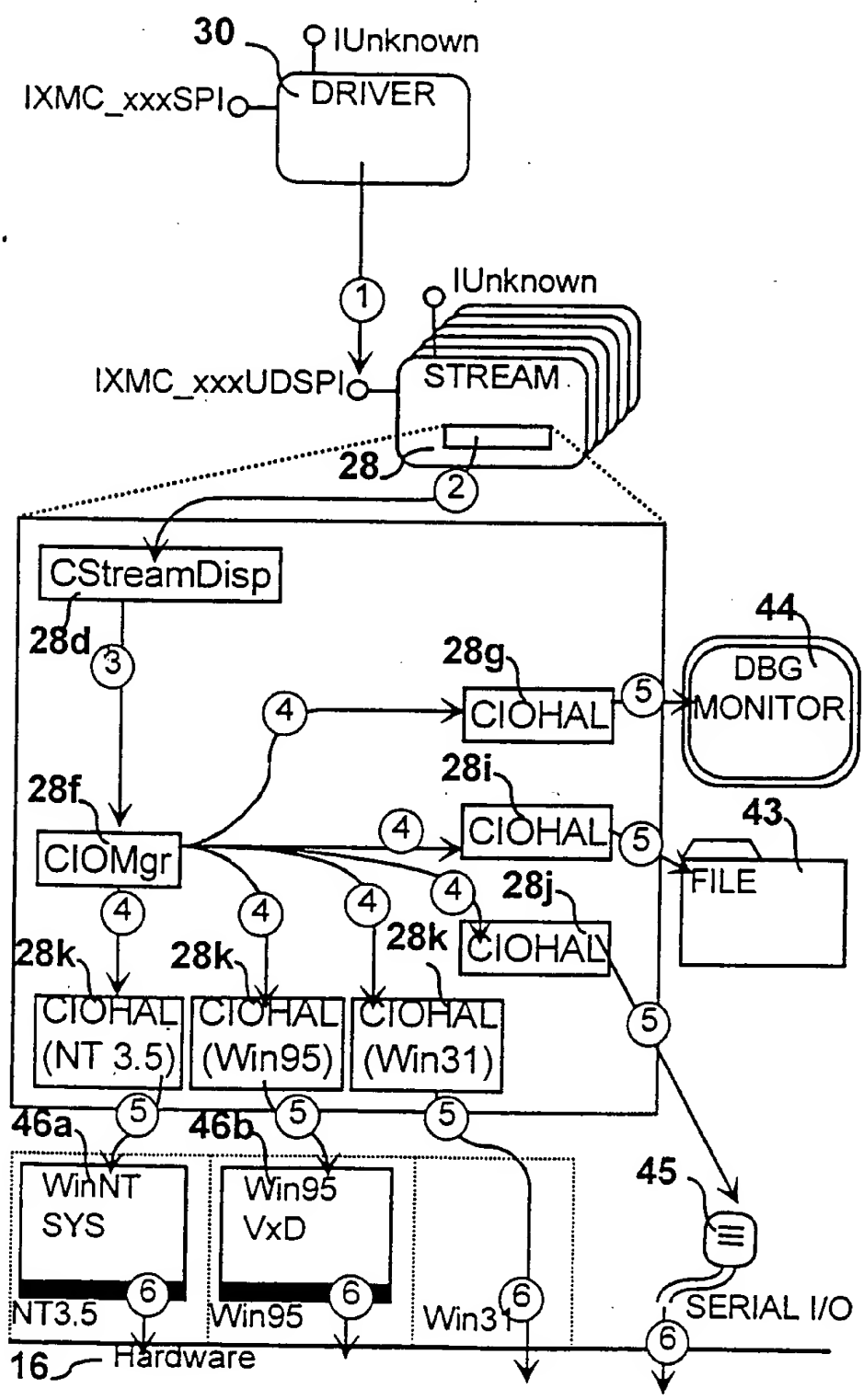


FIG. 32 Scenario-Map - Clean-up (Drv)



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FIG. 33 Interface-Map

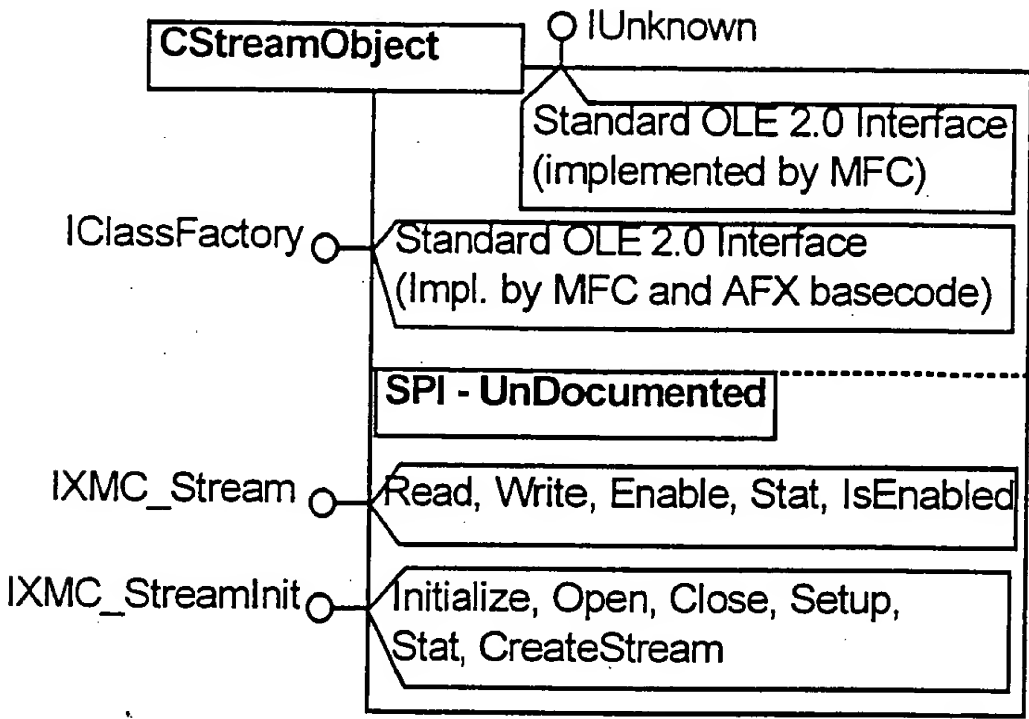


FIG. 34 Module Interaction-Map.

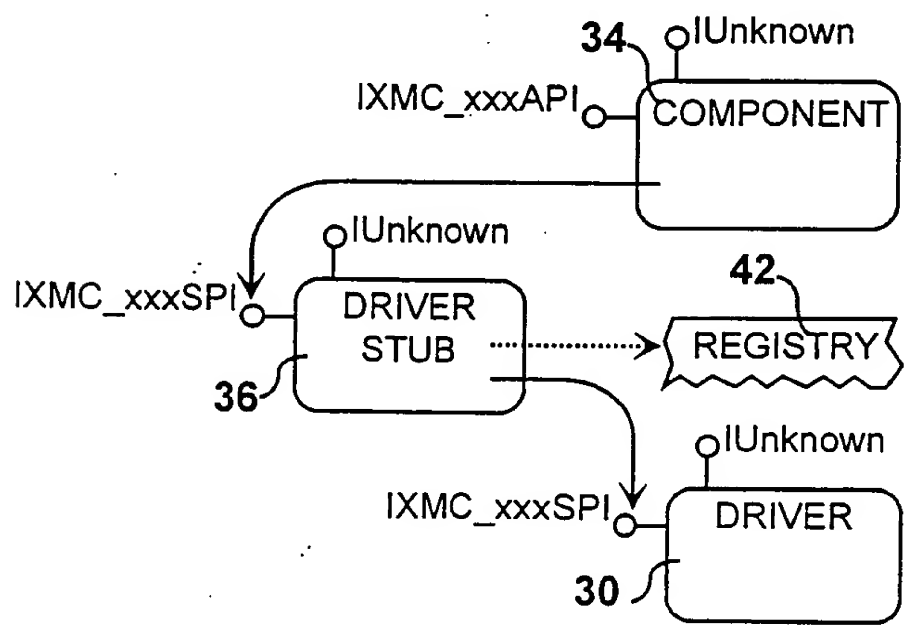
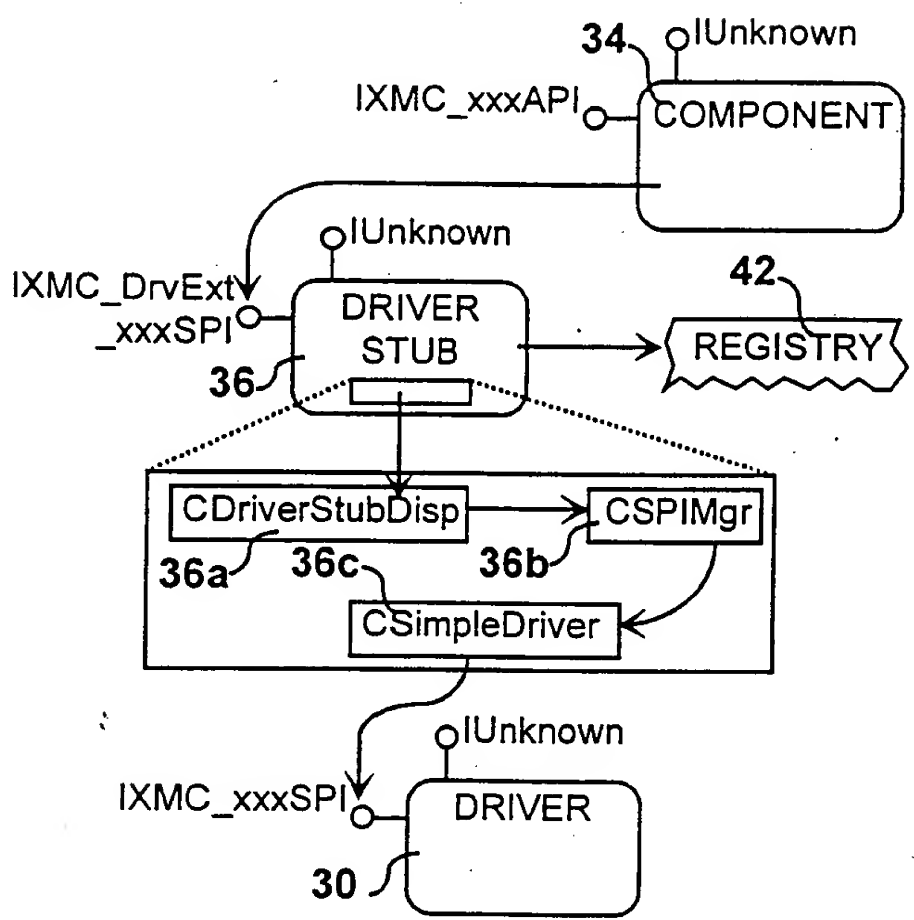
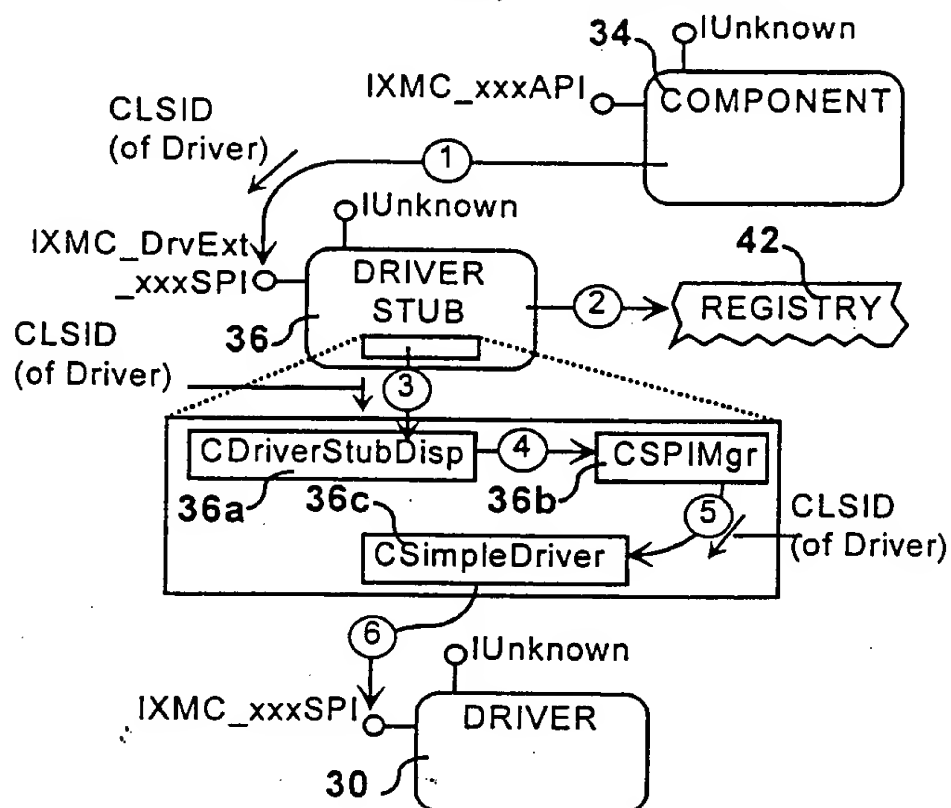


FIG. 35 Object Interaction-Map



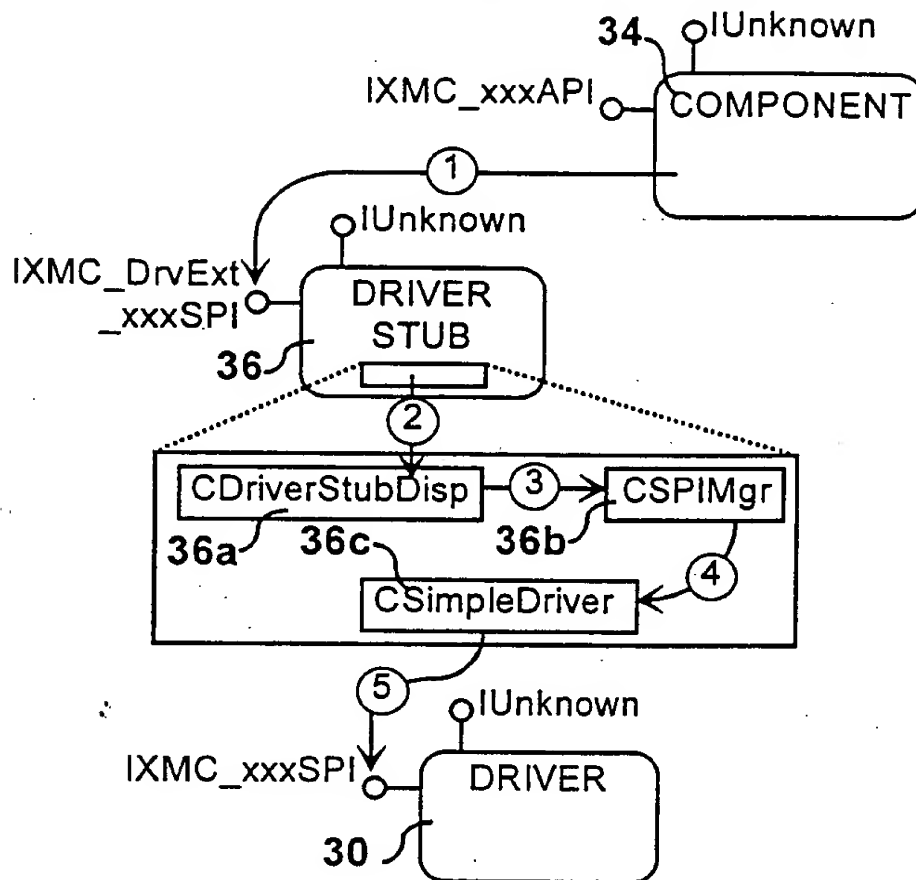
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FIG. 36 Scenario-Map - Initialization



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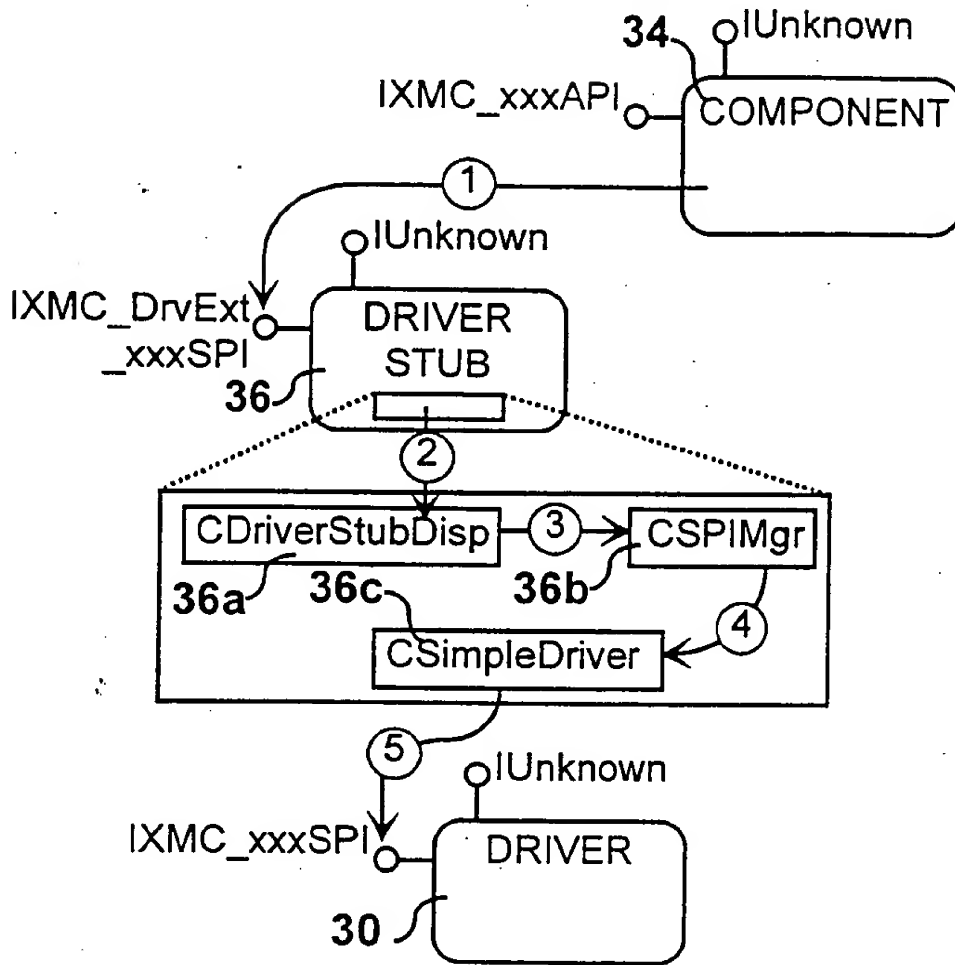
FIG. 37 Scenario-Map - Operations



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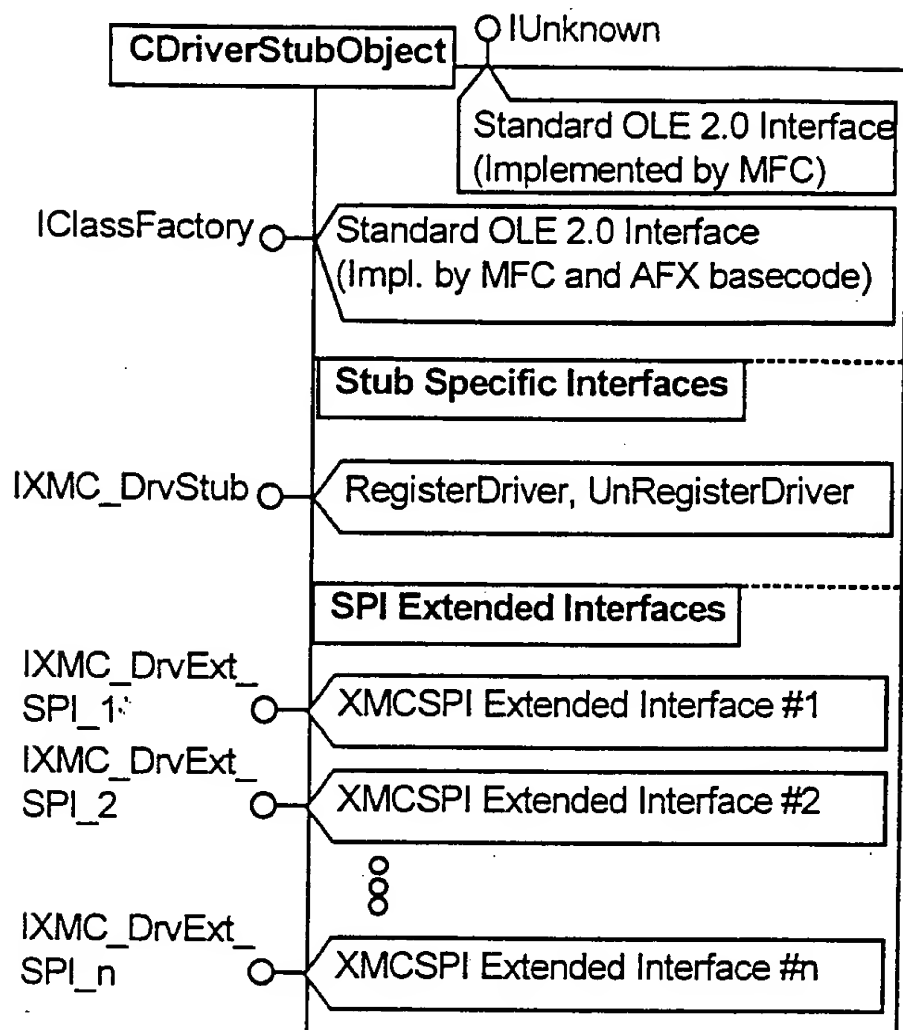
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FIG. 38 Scenario-Map - Clean-up



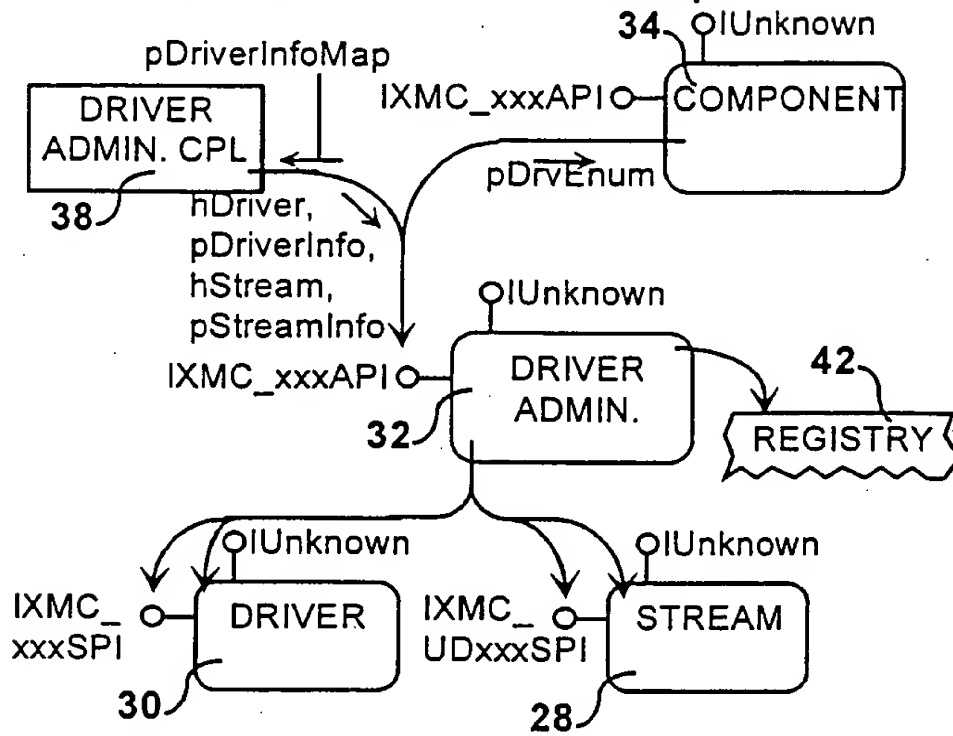
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FIG. 39 Interface-Map

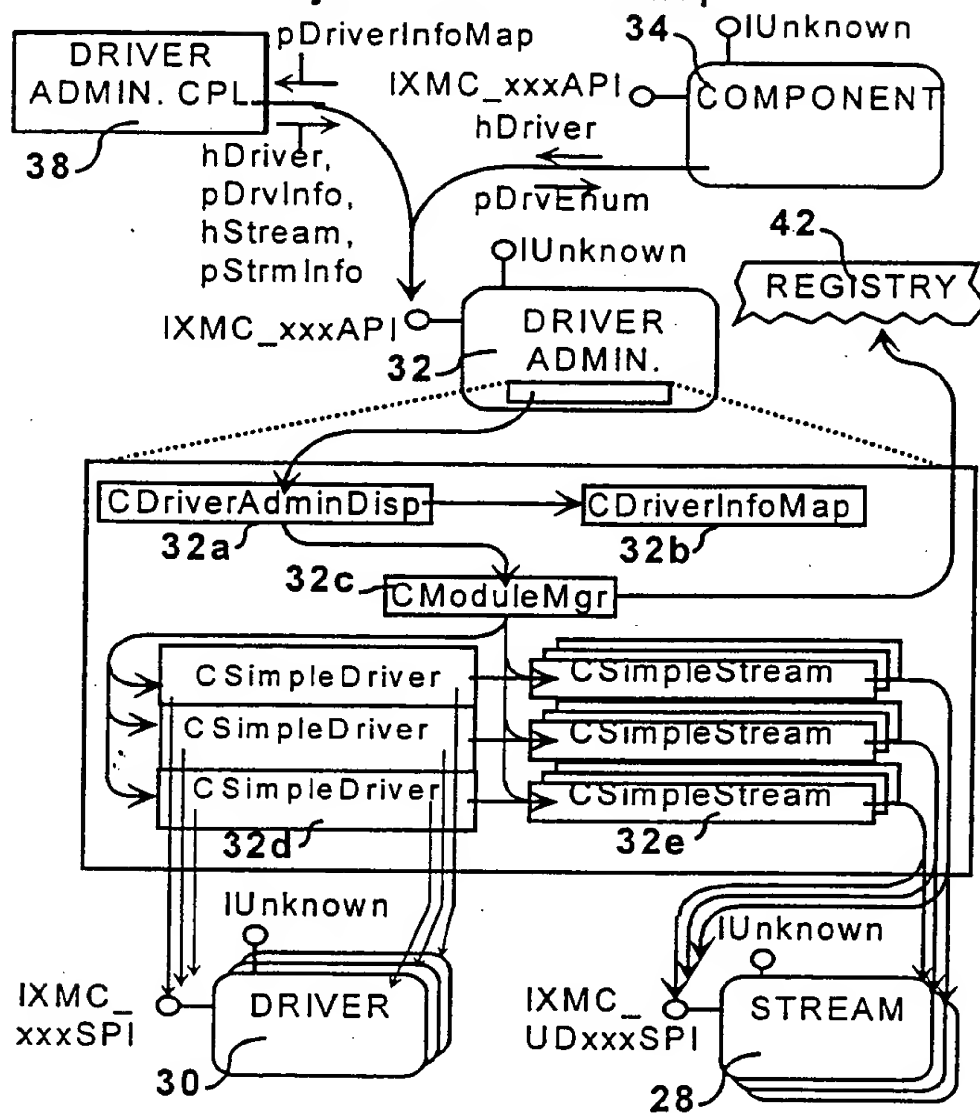


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FIG. 40 Module Interaction-Map

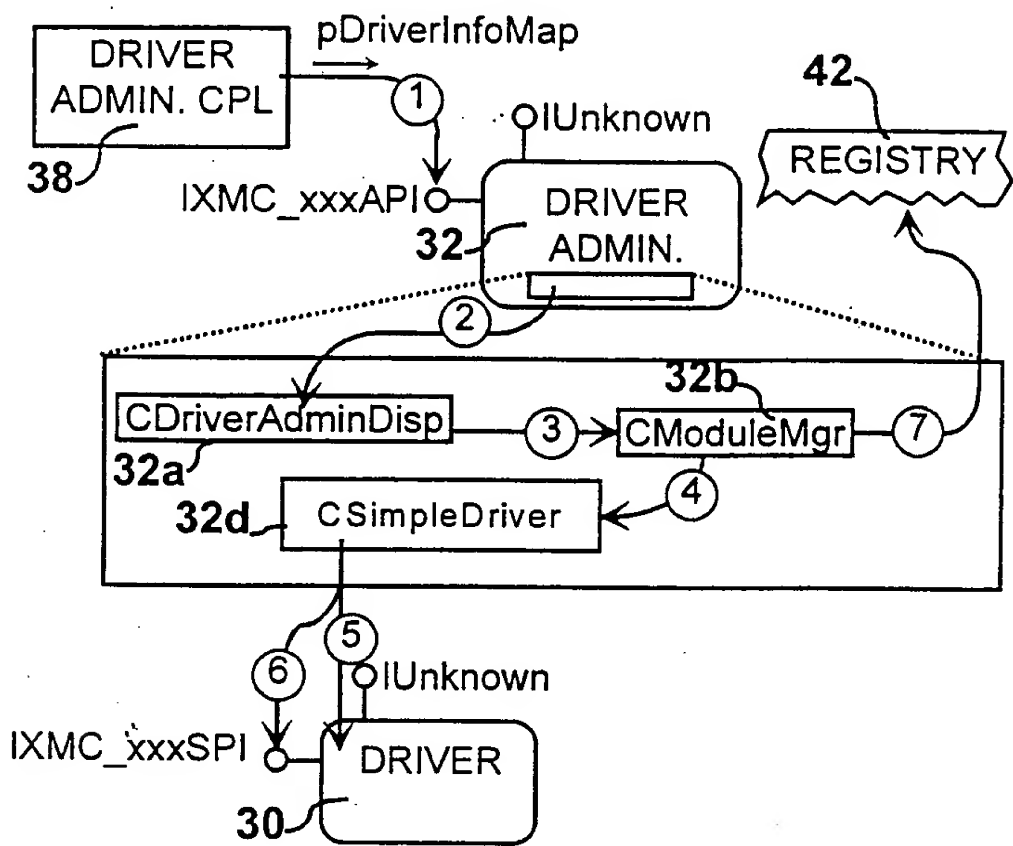


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[illegible]

[illegible]

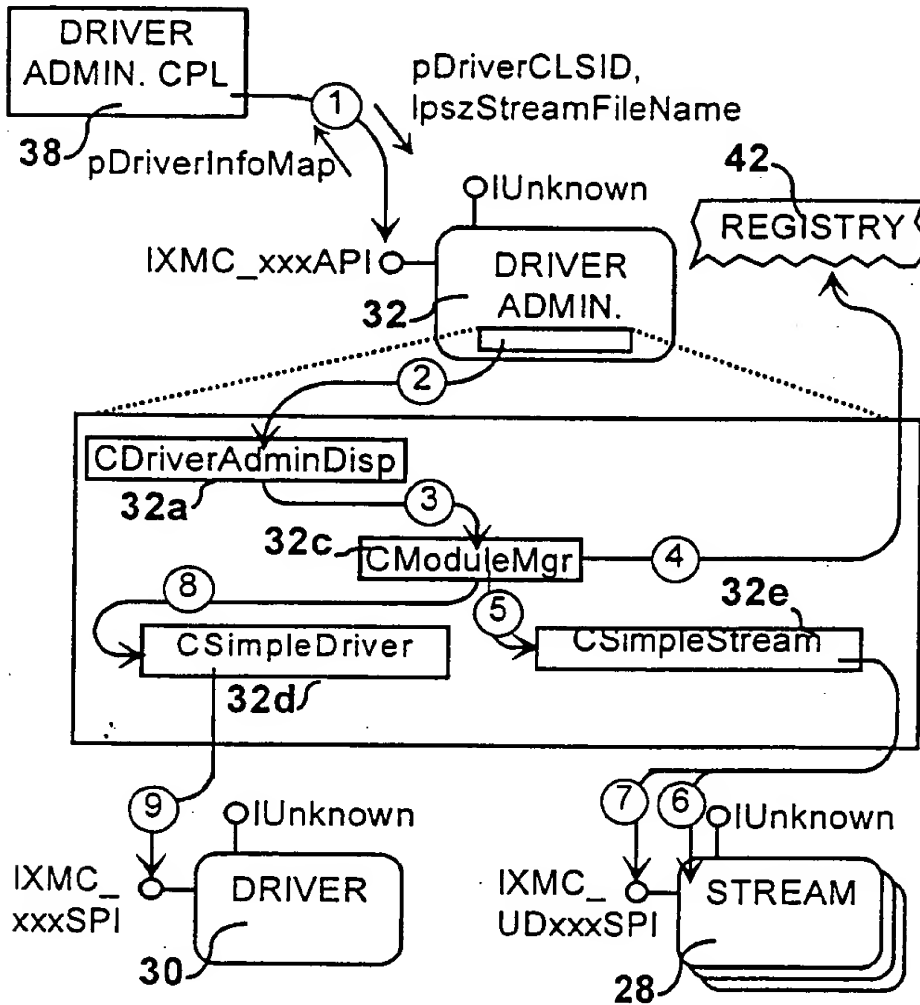
FIG. 43 Scenario-Map - Registering a Driver



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FIG. 44 Scenario-Map - Registering a Stream



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```

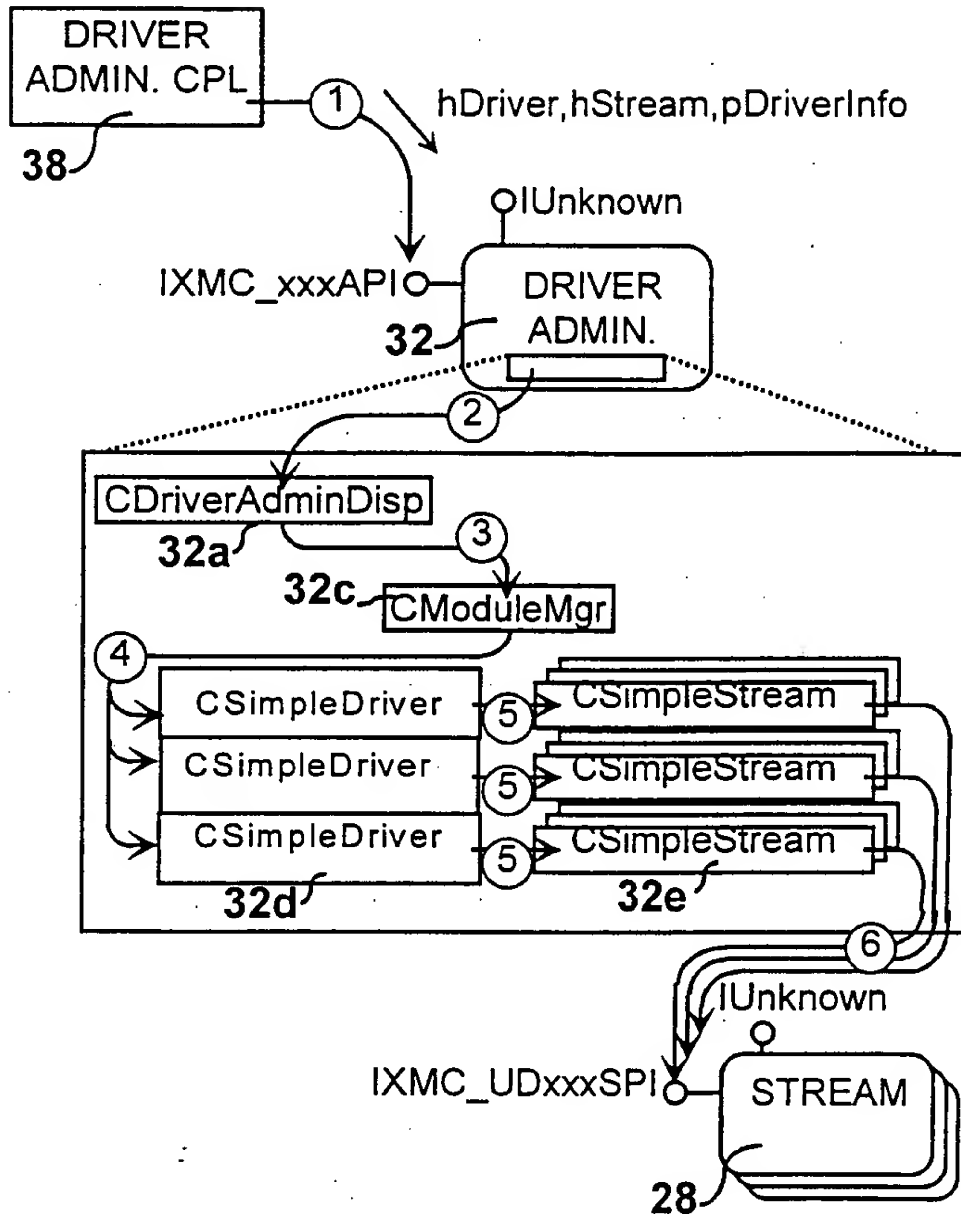
graph TD
    subgraph System
        CDA[CDriverAdminDisp 32a]
        CM[CModuleMgr 32c]
        subgraph Drivers
            CS1[CSimpleDriver]
            CS2[CSimpleDriver]
            CS3[CSimpleDriver]
        end
    end
    DA[DRIVER ADMIN. 32]
    DAC[DRIVER ADMIN. CPL 38]
    D[DRIVER 30]

    DAC -- "IXMC_xxxAPI 1, hDriver, pDrvInfo" --> DA
    DA -.-> CM
    CM -- "4" --> CS1
    CM -- "4" --> CS2
    CM -- "4" --> CS3
    CS1 -- "IXMC_xxxSPI 5, IUnknown" --> D
    DA -- "2" -.-> DA
    style DA fill:#fff,stroke:#000,stroke-width:1px
    style DAC fill:#fff,stroke:#000,stroke-width:1px
    style D fill:#fff,stroke:#000,stroke-width:1px
  
```

[illegible]

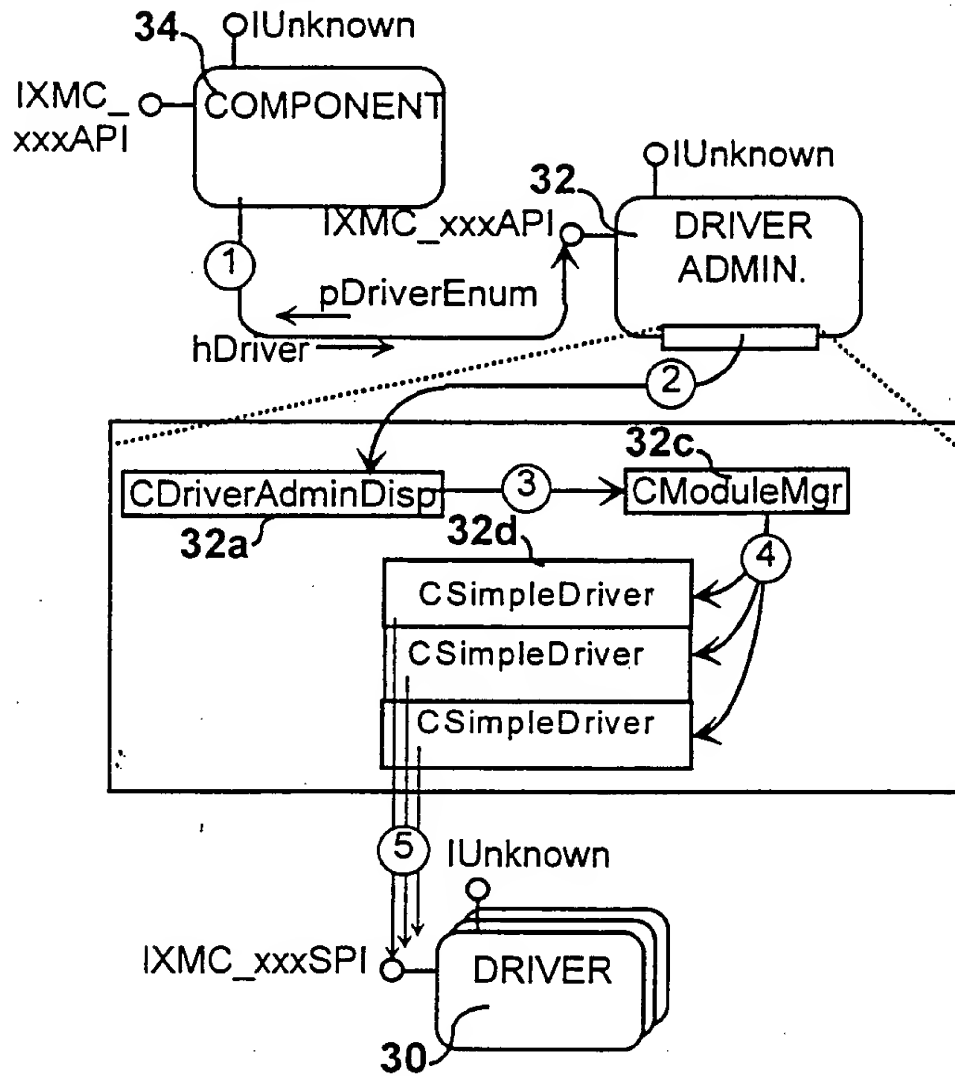
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FIG. 46 Scenario-Map - Setting Stream Info.



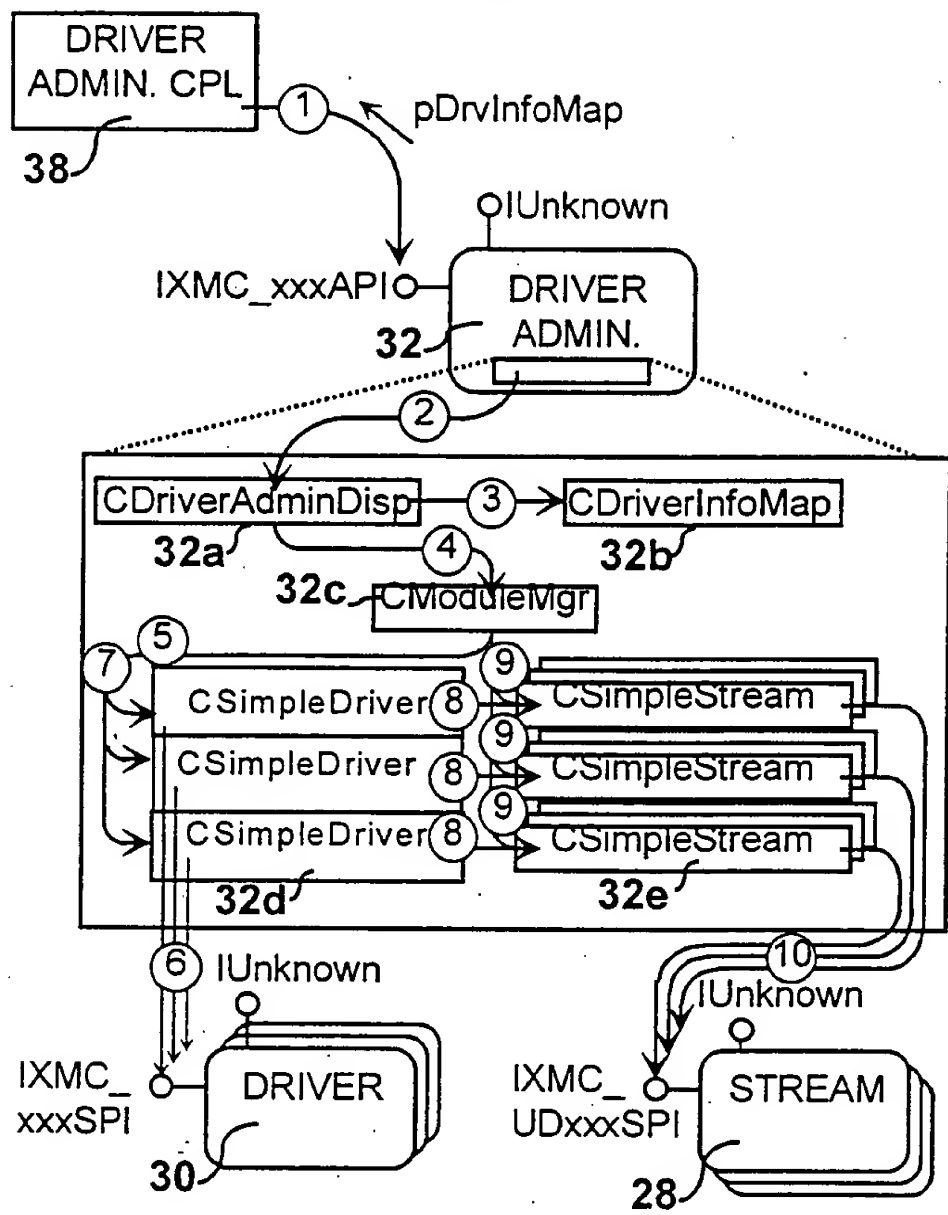
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FIG. 47 Scenario-Map - Querying Driver Enum.



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FIG. 48 Scenario-Map - Querying Drv Info. Map



000000-000000

[illegible]

FIG. 50 Interface-Map.

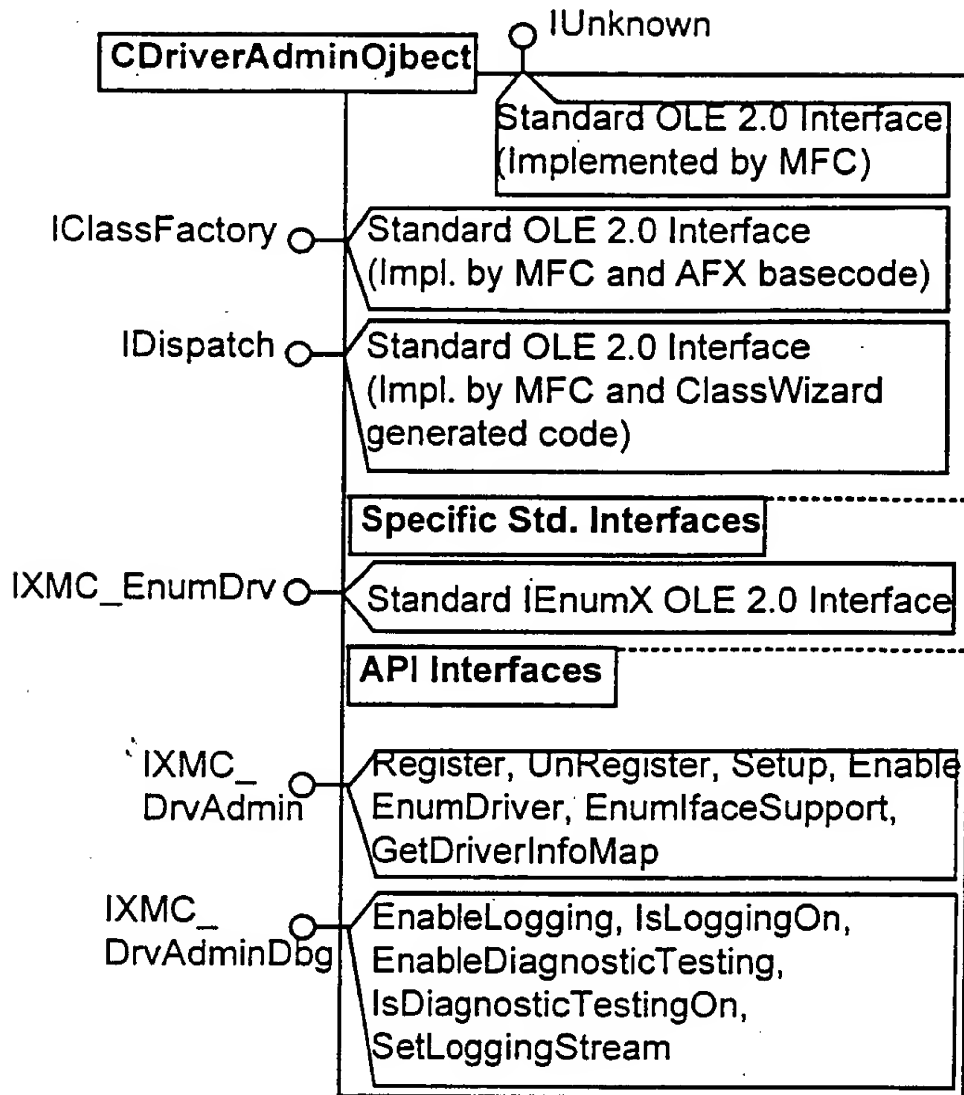
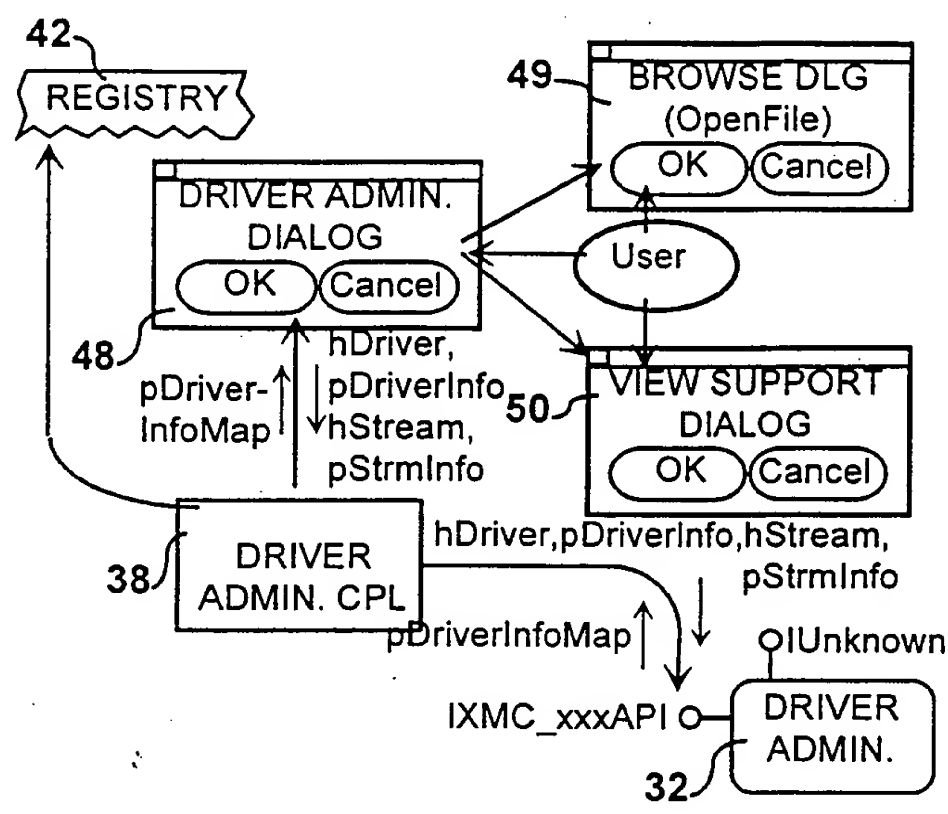


FIG. 51 Module Interaction-Map



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FIG. 52 Object Interaction-Map

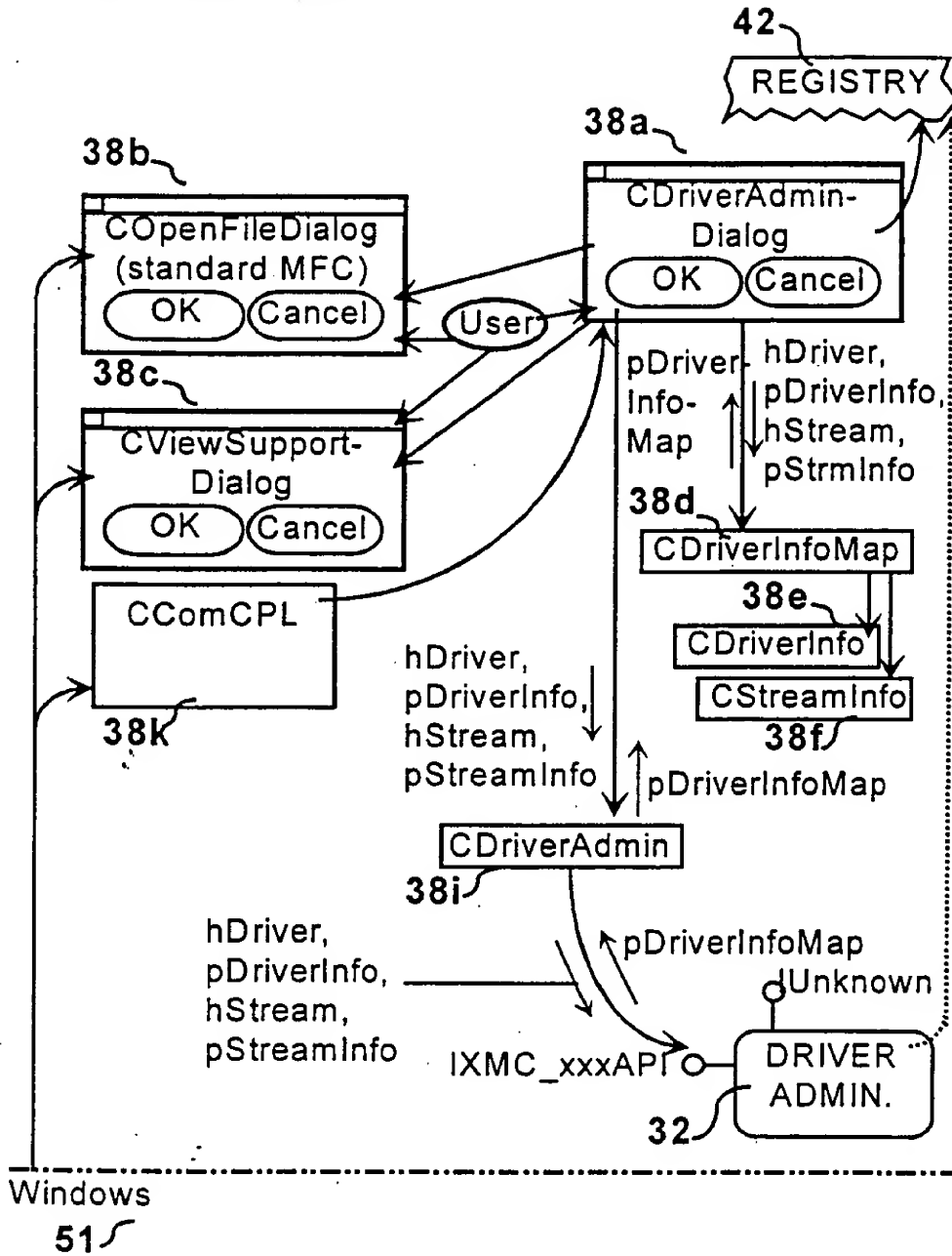
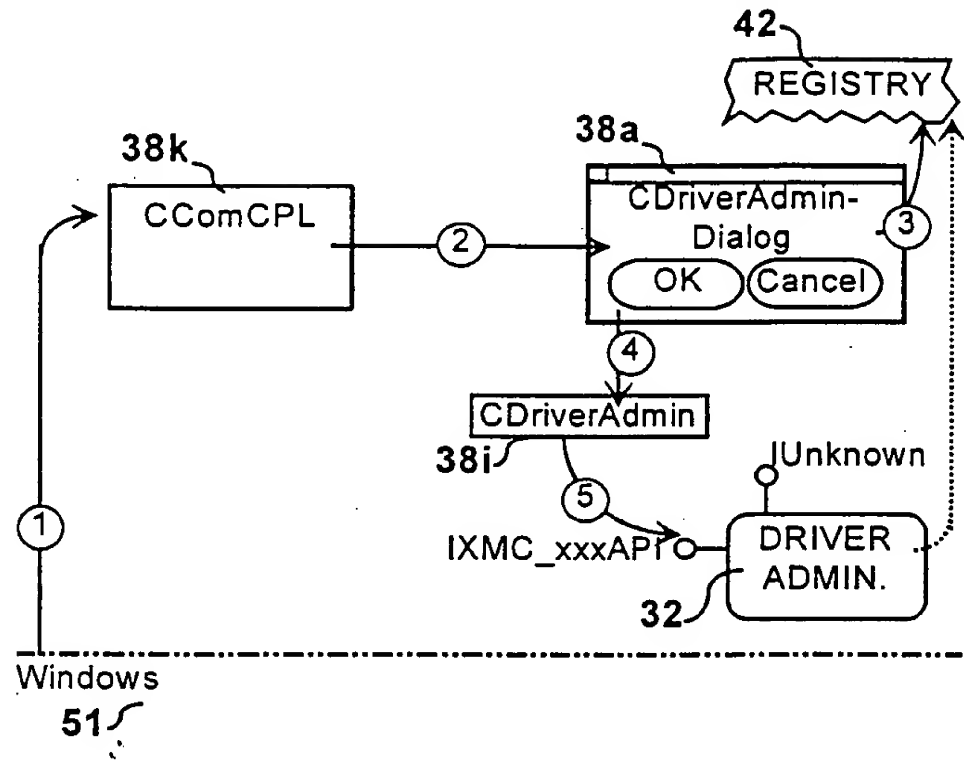


FIG. 53 Scenario-Map - Initializing the App.



```

graph TD
    38a[CDriverAdminDialog] -- 1 --> 38i[CDriverAdmin]
    38i -- 2 --> 32[DRIVER ADMIN.]
    38a -- 3 --> 38d[CDriverInfoMap]
    32 -- IXMC_XXXAPI --> 38i
    32 -- Unknown --> 32
  
```

Figure 1 consists of 12 micrographs arranged vertically, showing the stages of chick embryo development. The stages are labeled on the right side of the images: 1. Fertilization, 2. Cleavage, 3. Gastrulation, 4. Folding, 5. Folding, 6. Folding, 7. Folding, 8. Folding, 9. Folding, 10. Folding, 11. Folding, 12. Hatching. The images show the progression from a single cell to a fully formed chick in the egg.

FIG. 55 Scenario-Map - Adding a Driver

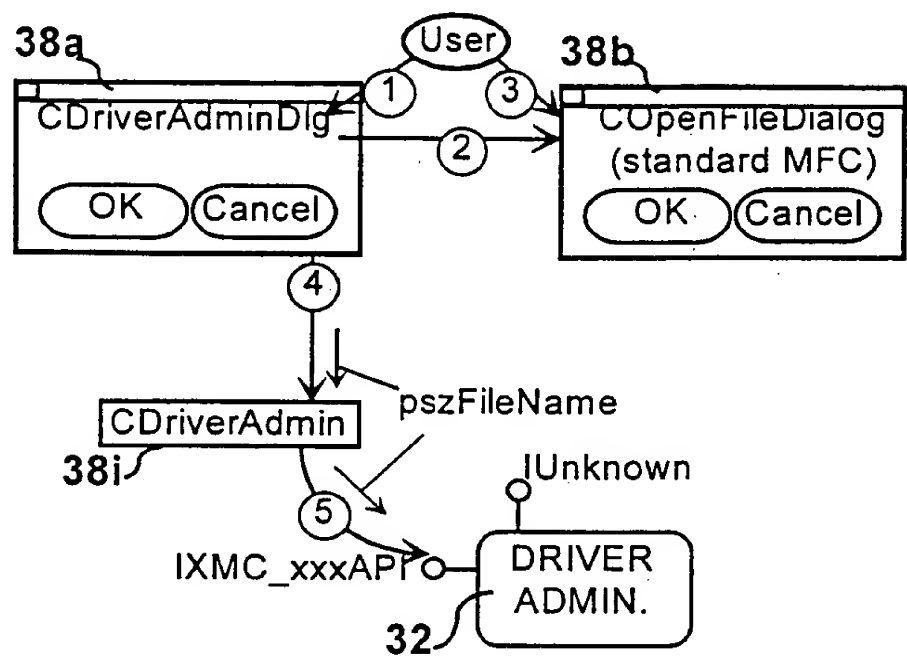
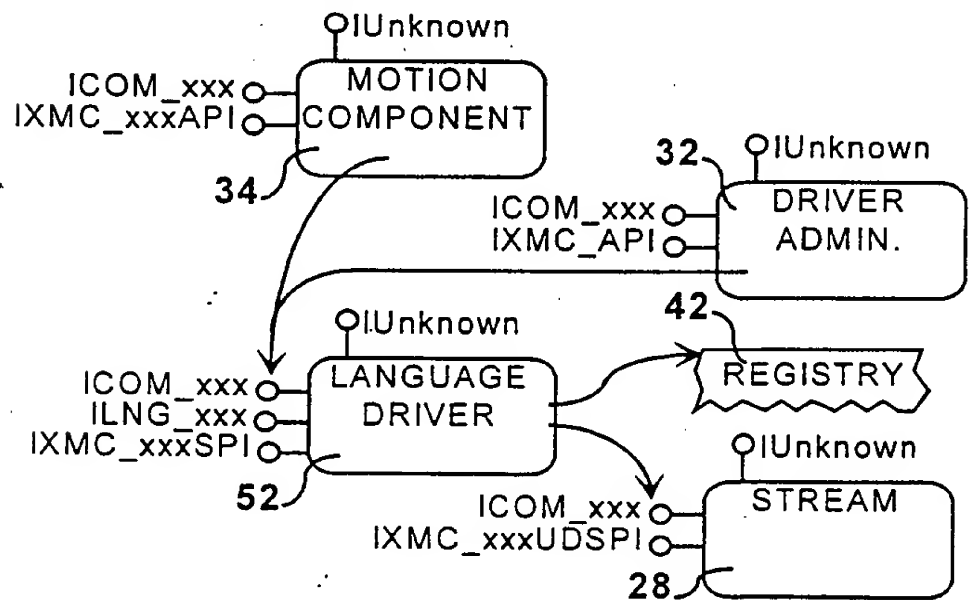


FIG. 58 Module Interaction-Map



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FIG. 29 Scenario-Map - Opening the Stream

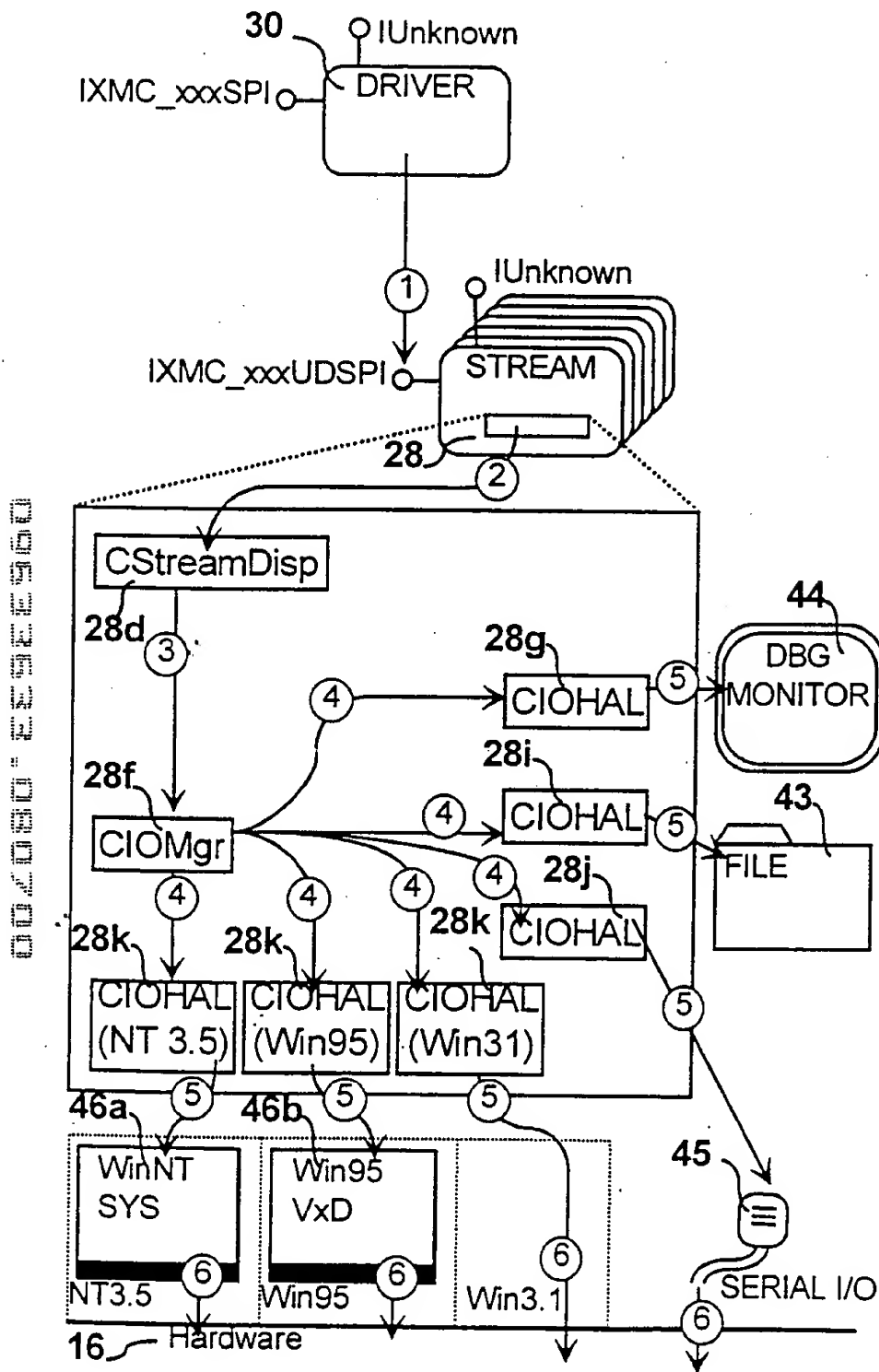


FIG. 56 Scenario-Map - Removing a Driver

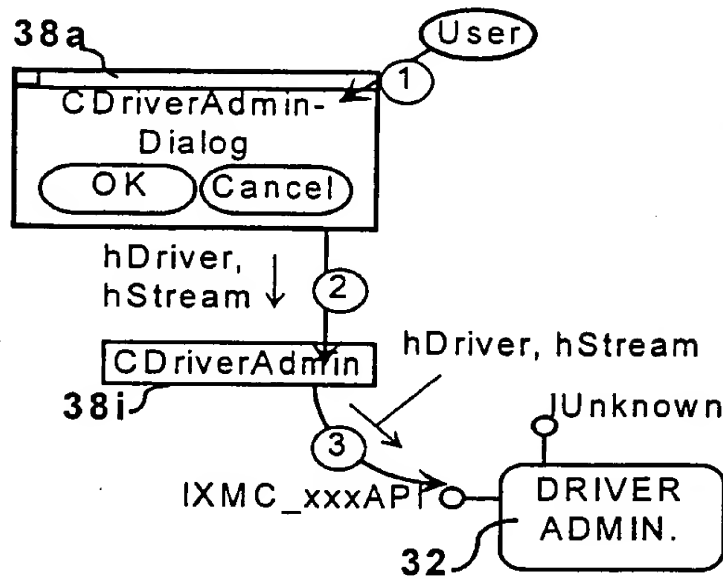


FIG. 57 Scenario-Map - View Support

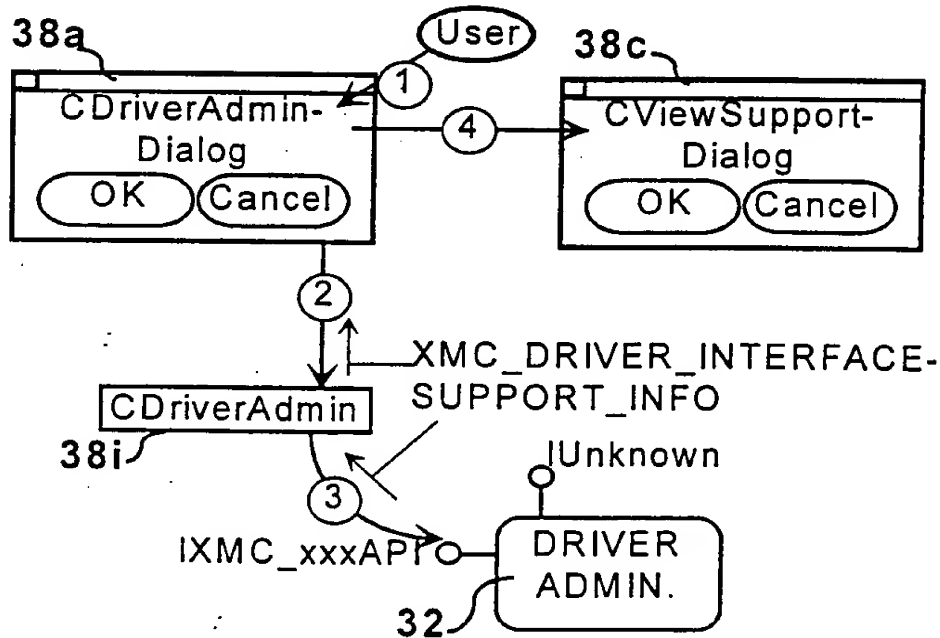
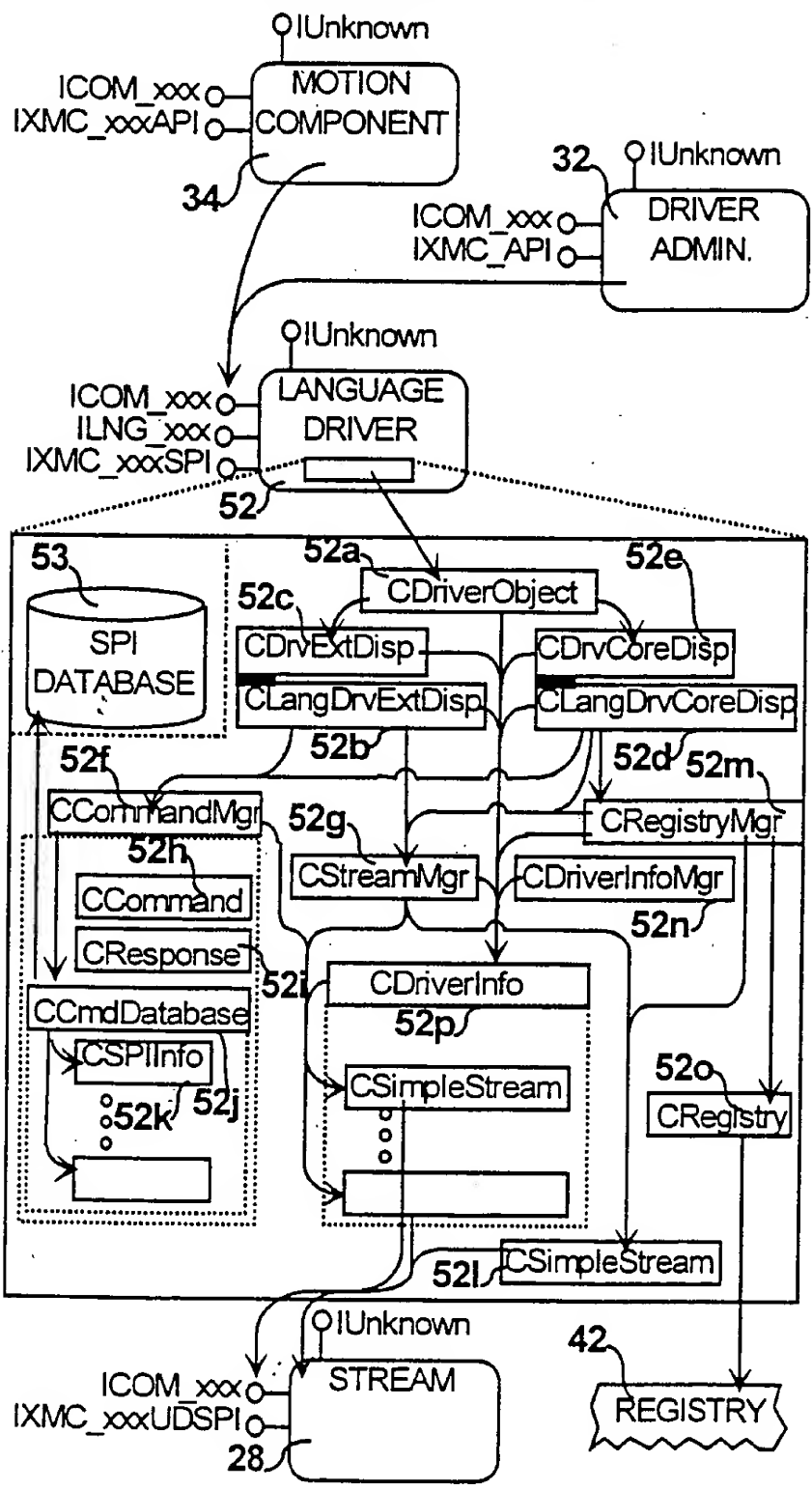


FIG. 67

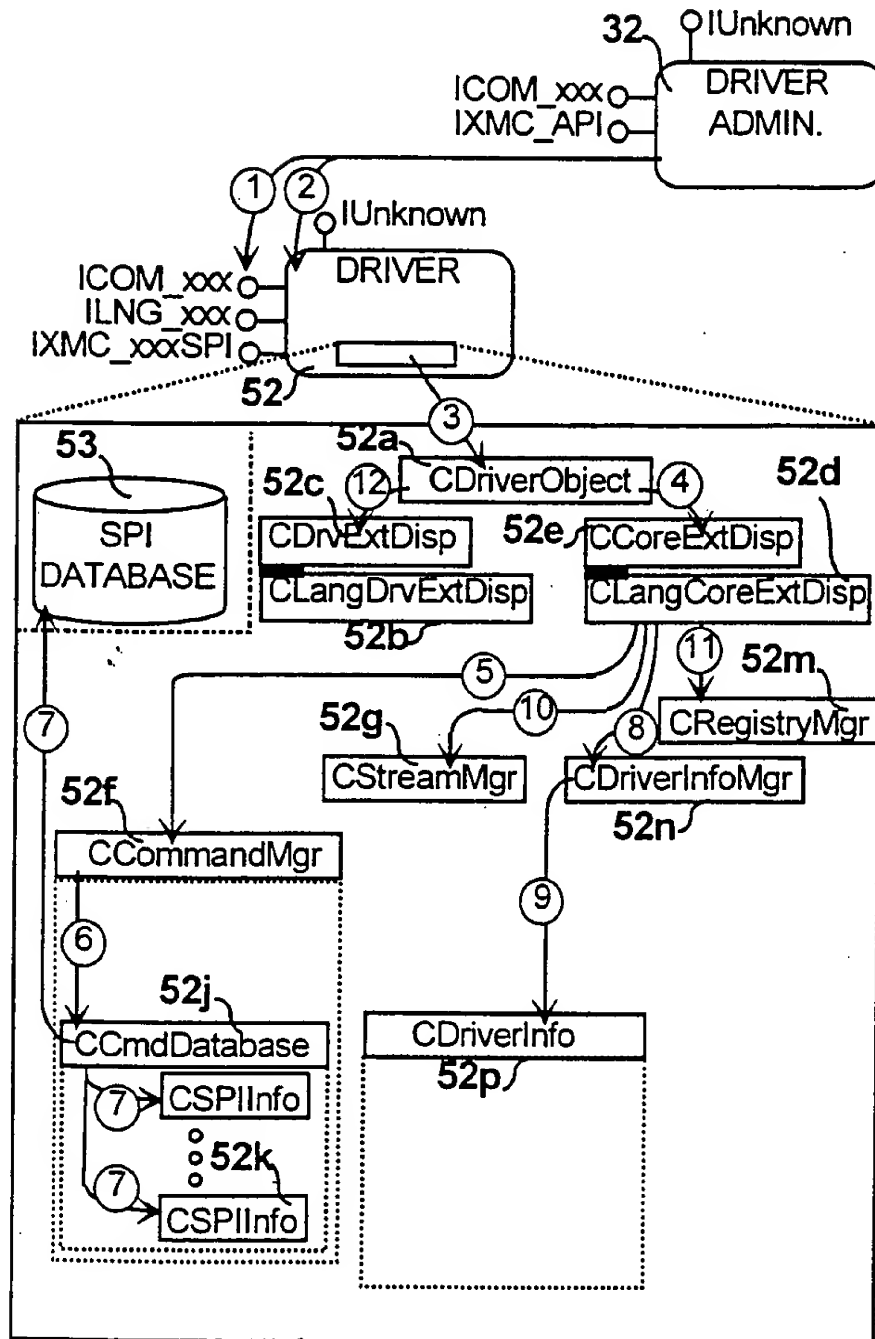
<Custom> SPI Database					
Date <date>	Name <name>	Company <company>	Hardware <hardware>	Command Data Types	Response Data Types
				%s = string %d = double %u = ULONG or DWORD %w = WORD %d = int %b = BOOL (1 or 0) + = continue previous type*	%s = string %d = double %u = ULONG or DWORD %w = WORD %d = int %b = BOOL (1 or 0) + = continue previous type*
NOTE: The '+' operator directs the parser to continue using the previous type until either: a.) The end of the formal string is reached, or b.) the next type changes from number to string or vice-versa				Command Macros @[snd]	Response Macros @[rcv] @[cr] @[ll] @[crl]
TODO: Replace the [Custom]::f00 entry with your own SPI interface(s), save the file as a Tab delimited text file, and import just the SPI data of the text file into your Resource File.					
NOTE: After copying the data below (between the COPY THIS DATA and END OF COPY) into your xxx_cmbd bin resource file, you MUST make sure that a '\0x0a' character followed by a '\0x0d' character are at the very beginning of the data. In other words, when copying the data from the text file, MAKE SURE to start your copy just after the last '>' character in the "...> COPY THIS DATA >..." text.					
Index	Interface Name	Function Name	HW Command Fmt	HW Response Fmt	TYPE
0	[Custom]	f00	NOP@[snd]	@[rcv]	EXT
IMPLEMENTATION					
DIRECT					

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FIG. 59 Object Interaction-Map

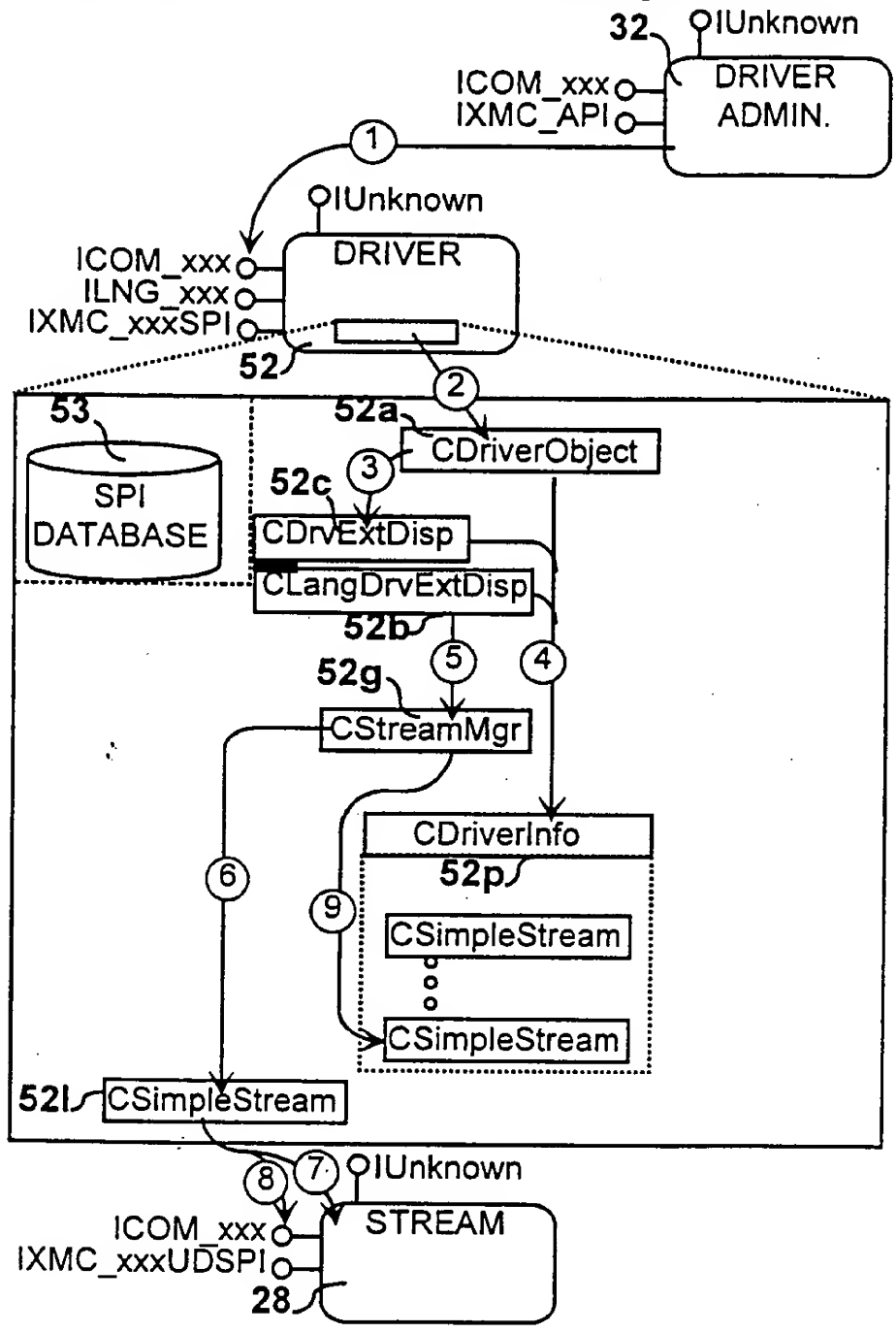


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FIG. 60 Scenario-Map - Init. by Drv. Admin.

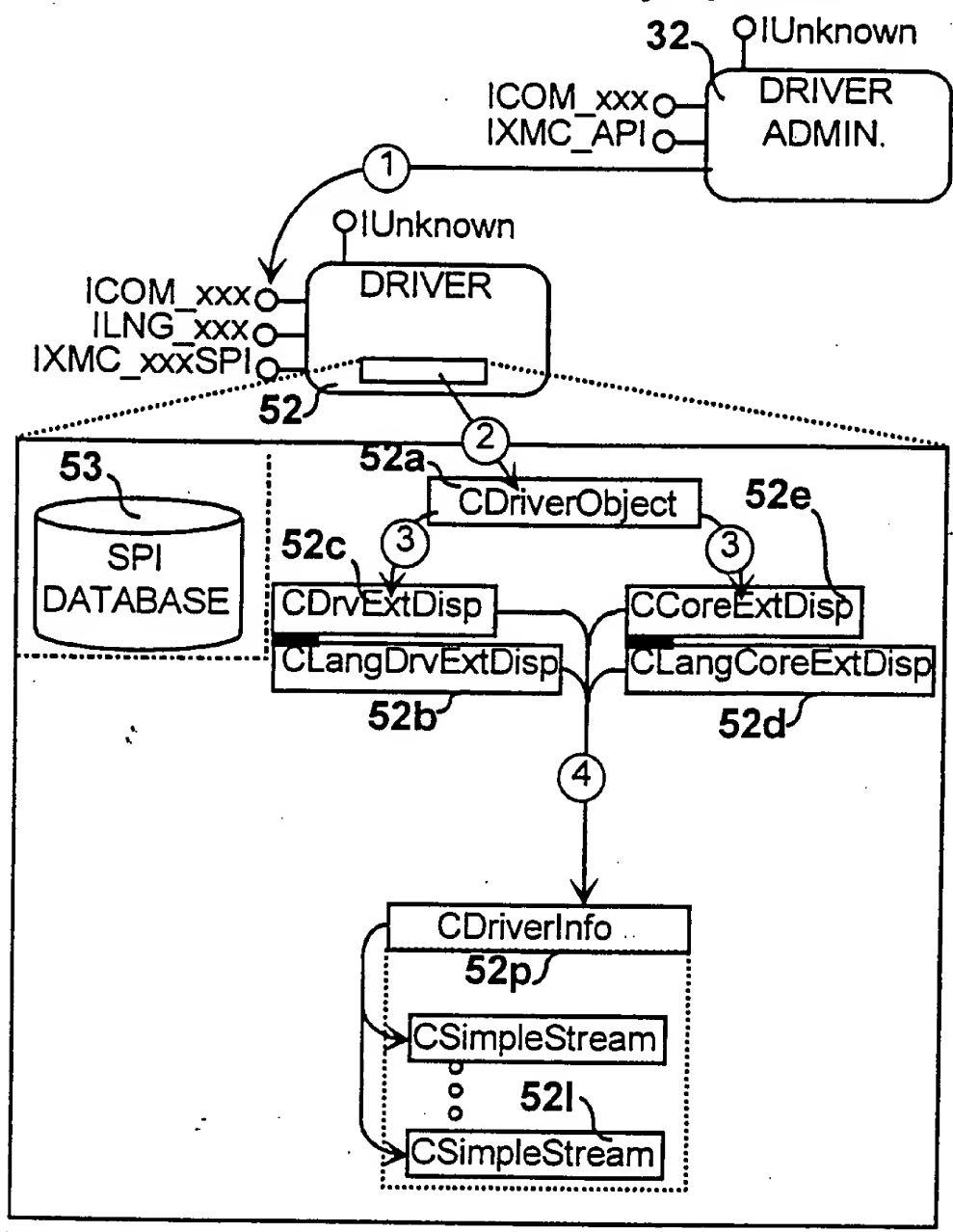
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FIG. 61 Scenario-Map - Adding a Stream



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FIG. 62 Scenario-Map - Query Operation



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FIG. 63 Scenario-Map - Registration loading

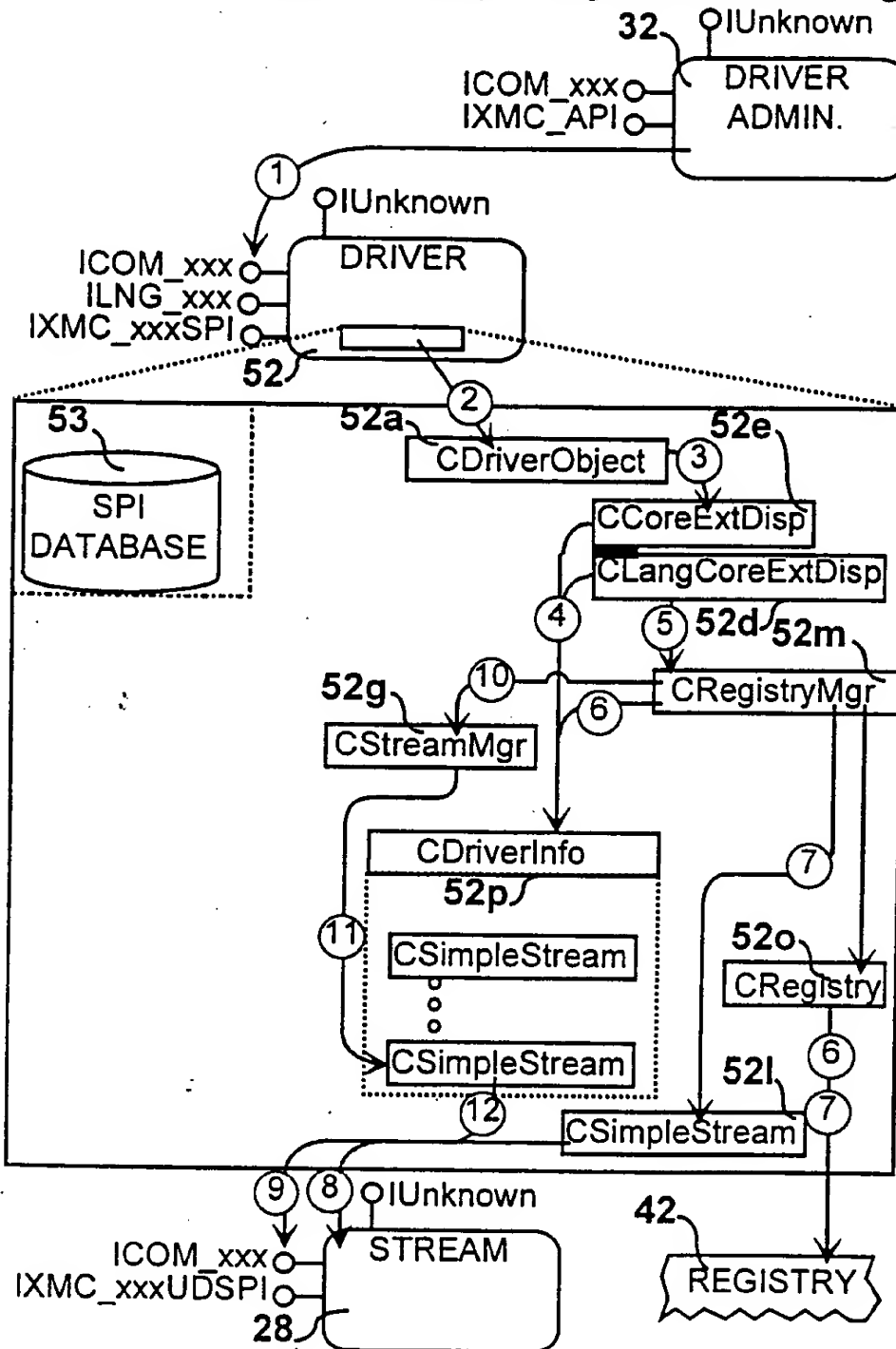


Figure 1 is a block diagram illustrating the system architecture. The diagram shows the following components and their interconnections:

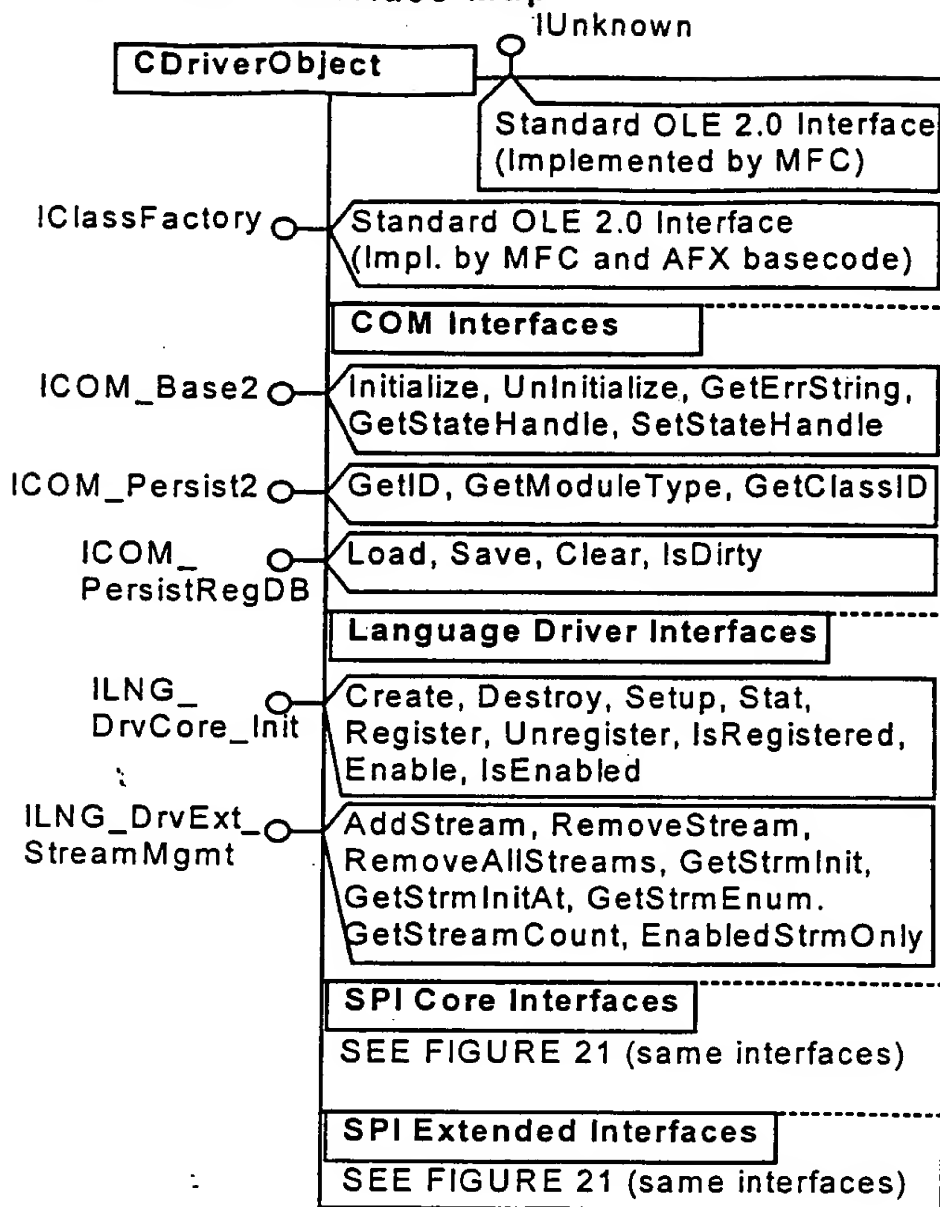
- External Components:**
 - MOTION COMPONENT:** Receives `ICOM_xxx` and `IXMC_xxxAPI` signals. It is connected to the **DRIVER** via a signal labeled 34.
 - DRIVER ADMIN.:** Receives `ICOM_xxx` and `IXMC_API` signals. It is connected to the **DRIVER** via a signal labeled 32.
 - DRIVER:** Receives `ICOM_xxx`, `ILNG_xxx`, and `IXMC_xxxSPI` signals. It is connected to the **STREAM** component via a signal labeled 52.
 - STREAM:** Receives `ICOM_xxx` and `IXMC_xxxUDSPI` signals. It is connected to the **CCmdDatabase** via a signal labeled 28.
- Internal Components (52):**
 - CDriverObject (52a):** Receives signal 2 from the **DRIVER**. It is connected to **CDrvExtDisp (52c)**, **CCoreExtDisp (52d)**, **CStreamMgr (52g)**, and **CDriverInfoMgr (52n)**.
 - CDrvExtDisp (52c):** Connected to **CLangDrvExtDisp (52b)**.
 - CCoreExtDisp (52d):** Connected to **CLangCoreExtDisp (52e)**.
 - CCommandMgr (52f):** Connected to **CCmdDatabase (52j)** via signal 5.
 - CCmdDatabase (52j):** Contains **CSPIInfo (52k)** and **CSPInfo (52l)**.
 - CStreamMgr (52g):** Connected to **CDriverInfo (52p)** via signal 6.
 - CDriverInfo (52p):** Contains **CSimpleStream (52i)** and **CStreamMgr (52g)**.
 - CRegistryMgr (52m):** Connected to **CDriverInfoMgr (52n)** via signal 6.

Figure 1 is a block diagram illustrating the system architecture. The diagram shows the following components and their interconnections:

- MOTION COMPONENT (34)**: Contains interfaces *ICOM_xxx*, *IXMC_xxxAPI*, and *UIUnknown*. It is connected to the DRIVER via a line labeled (1).
- DRIVER**: Contains interfaces *ICOM_xxx*, *ILNG_xxx*, and *IXMC_xxxSPI*. It is connected to the MOTION COMPONENT via line (1) and to the **CDriverObject** via line (2).
- CDriverObject (3)**: A central object that interacts with **CDrvExtDisp** (52c), **CLangDrvExtDisp** (52b), **CCoreExtDisp** (52e), and **CLangCoreExtDisp** (52d) via lines (3) and (5).
- CCmdMgr (52f)**: Contains **CCommand** (52h), **CResponse** (52i), and **CCmdDatabase** (52j). It is connected to the DRIVER via line (6) and to the **CDriverInfo** via line (7).
- CDriverInfo (52p)**: Contains **CSimpleStream** (52l) and an unnamed component (52m). It is connected to the **CCmdMgr** via line (7) and to the **STREAM** via line (8).
- STREAM (28)**: Contains interfaces *ICOM_xxx* and *IXMC_xxxUDSPI*. It is connected to the **CDriverInfo** via line (8) and to the **CCmdMgr** via line (10).

The diagram uses numbered lines (1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11) to indicate the flow of data or control between the components. Dashed lines indicate optional or indirect connections.

FIG. 66 Interface-Map



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